

FIG. 1

10

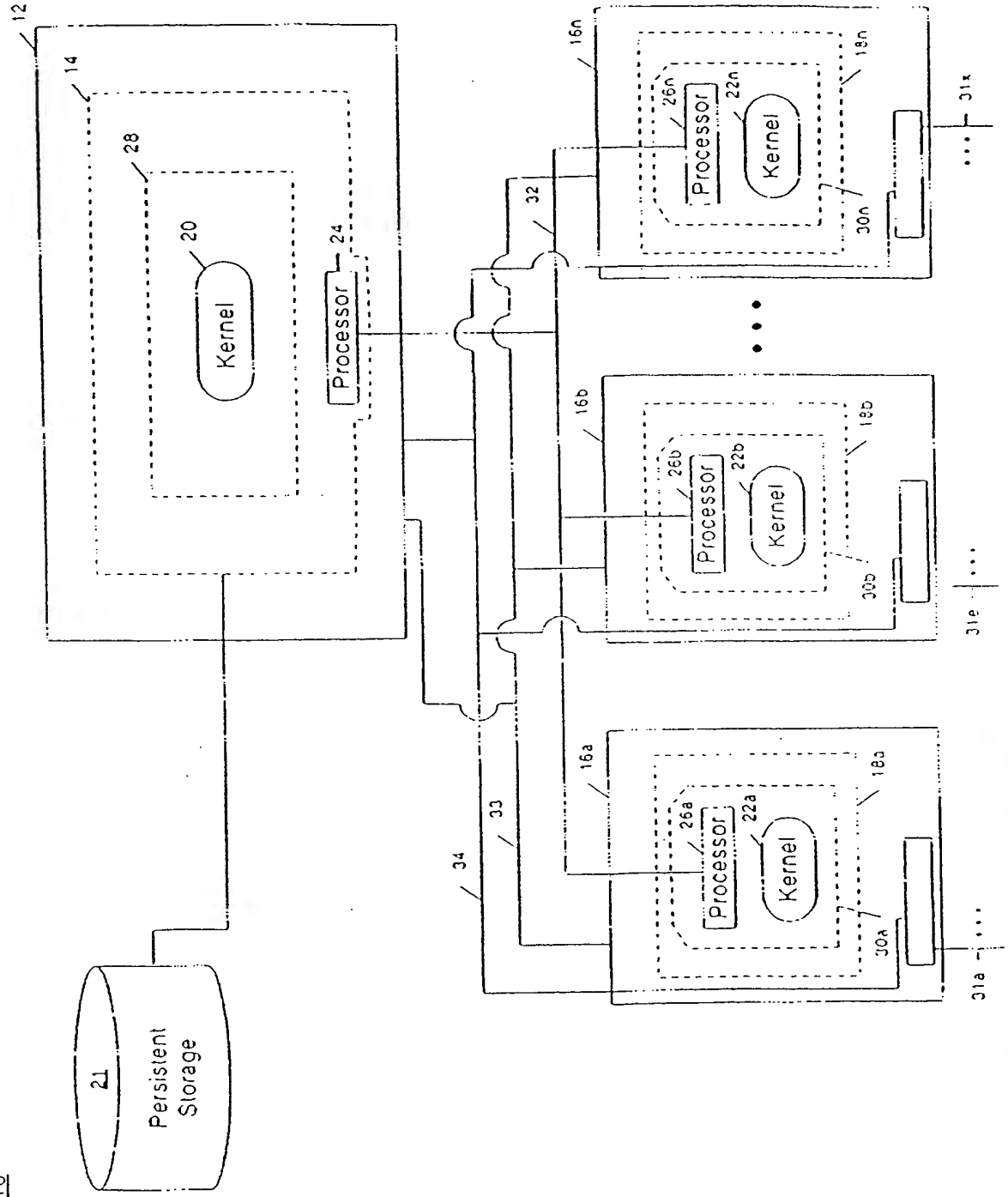


Fig. 2a

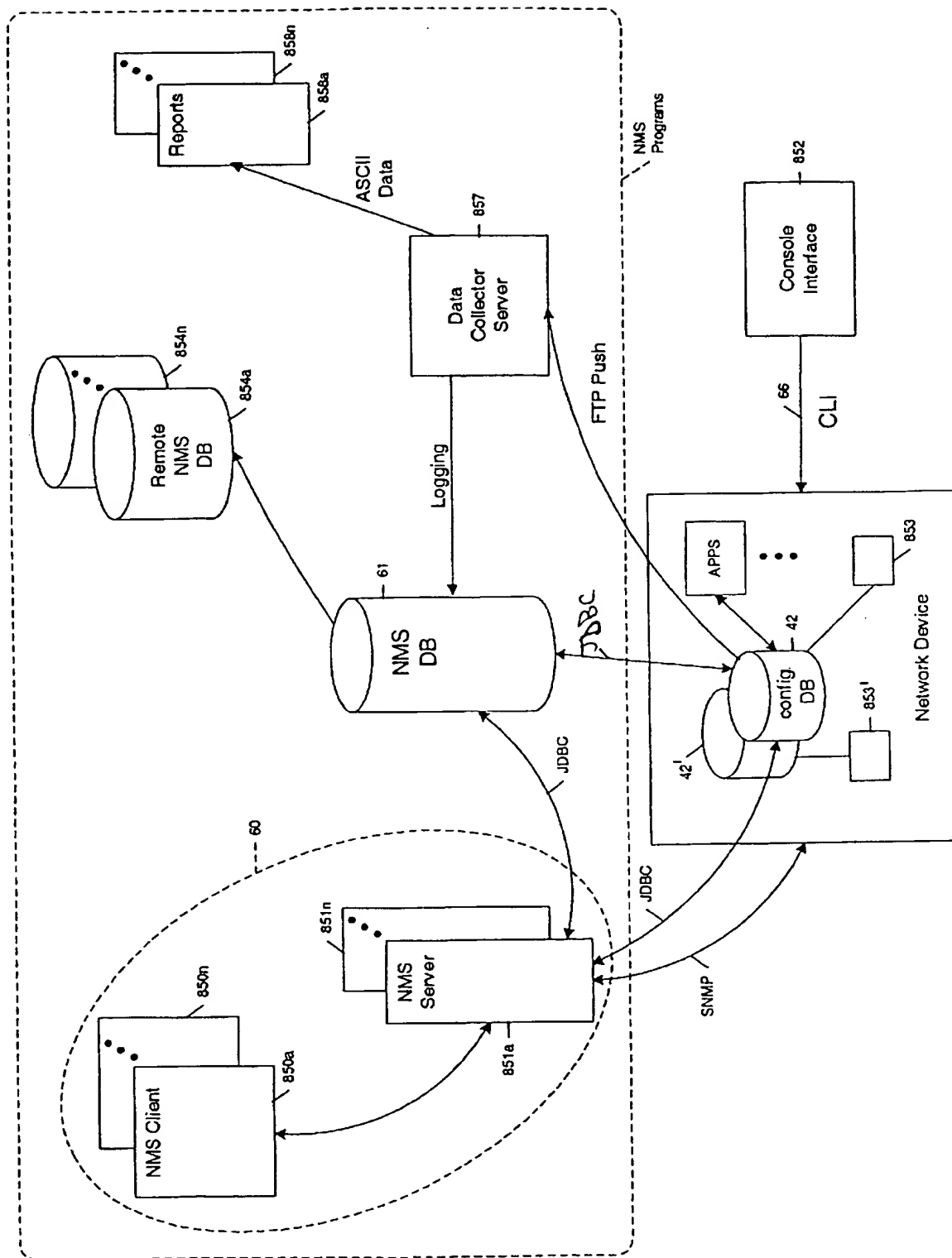
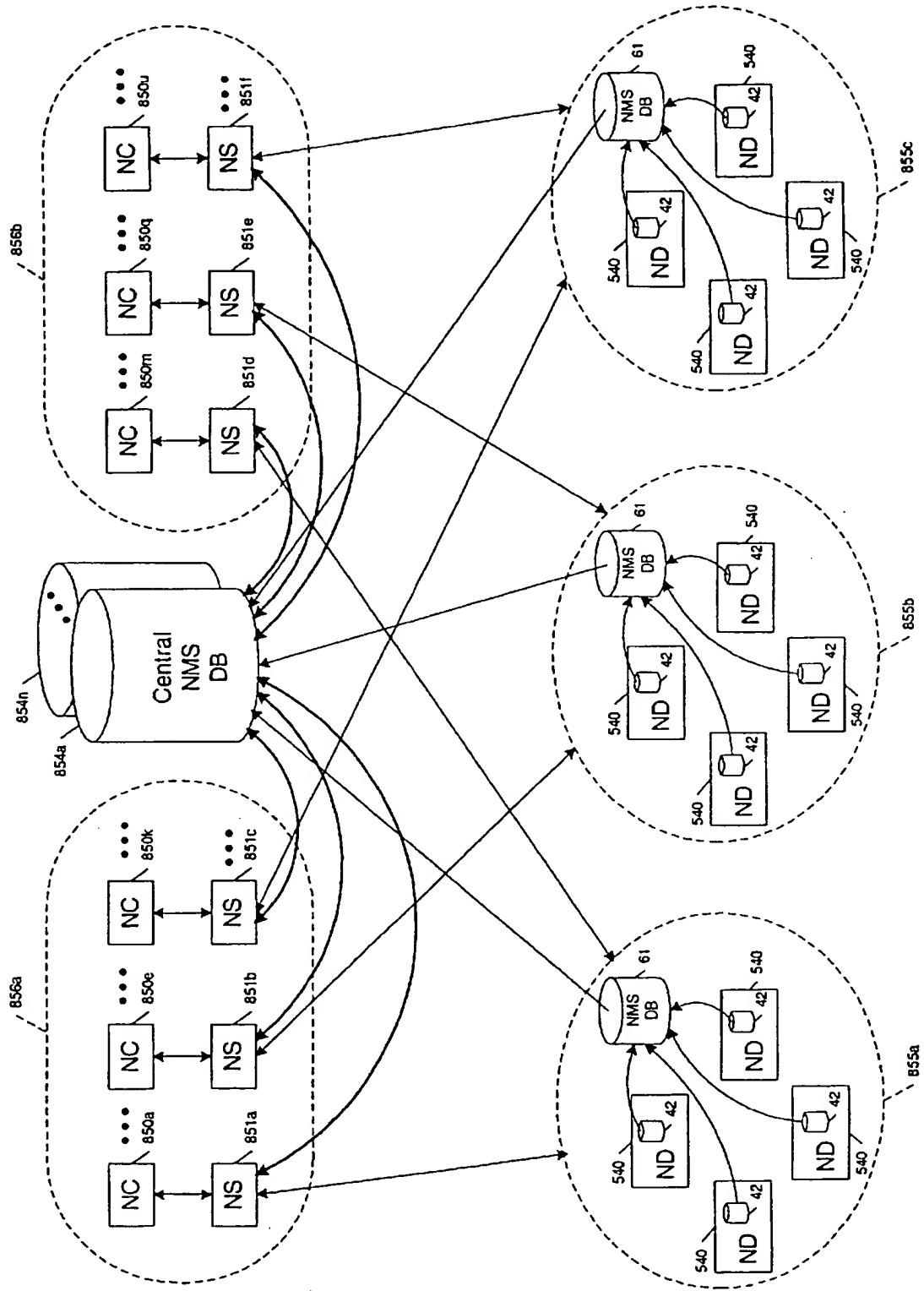
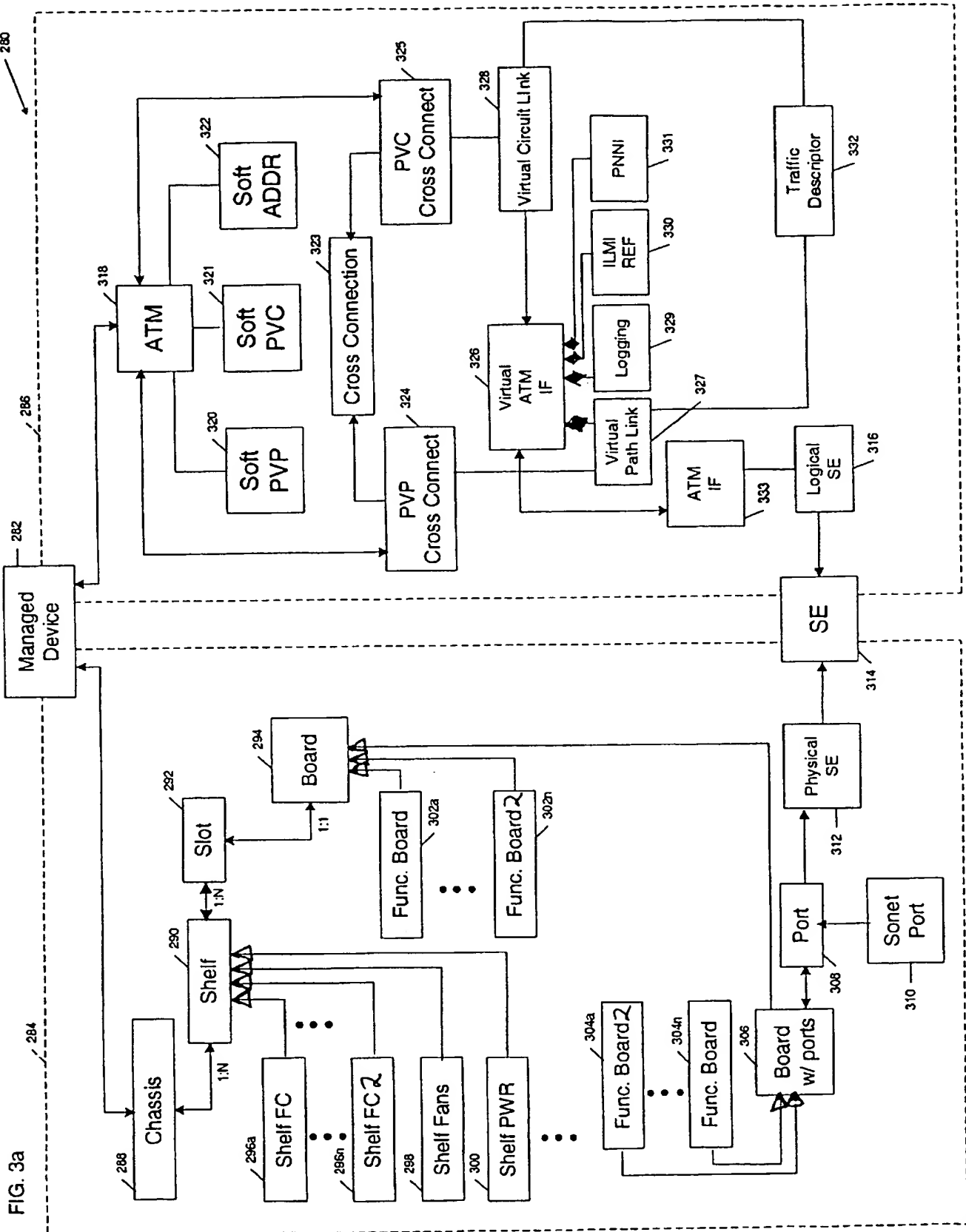
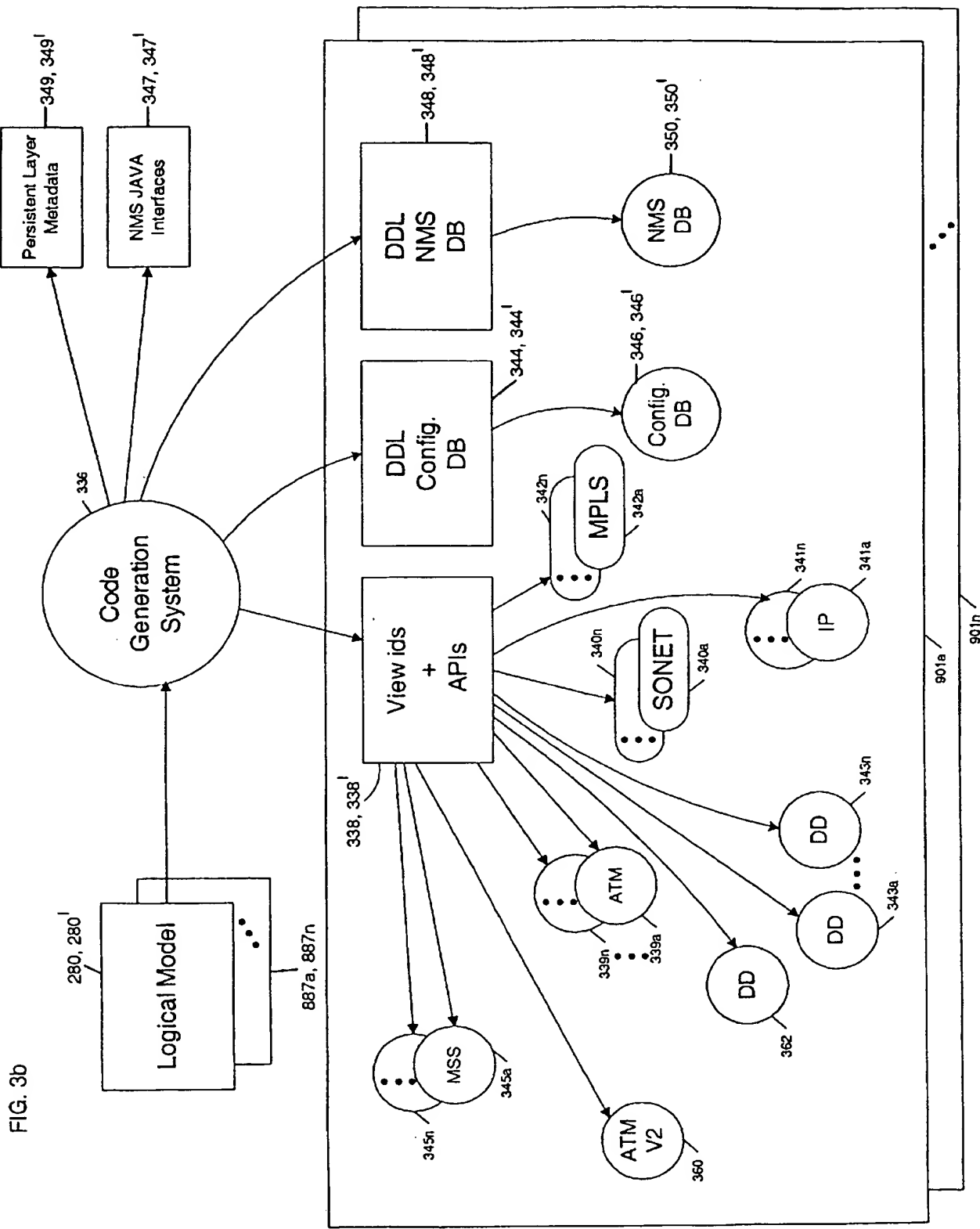


Fig. 2b







00T120" 002/E950

FIG. 3c

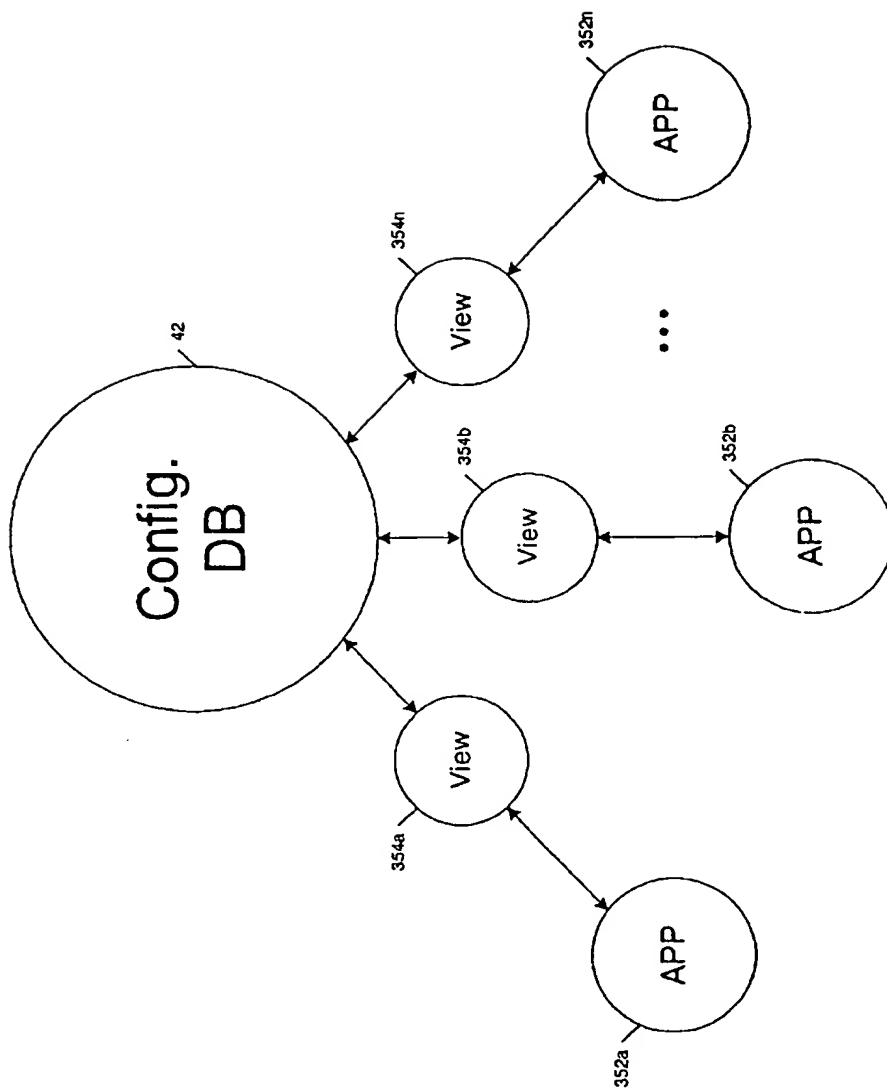
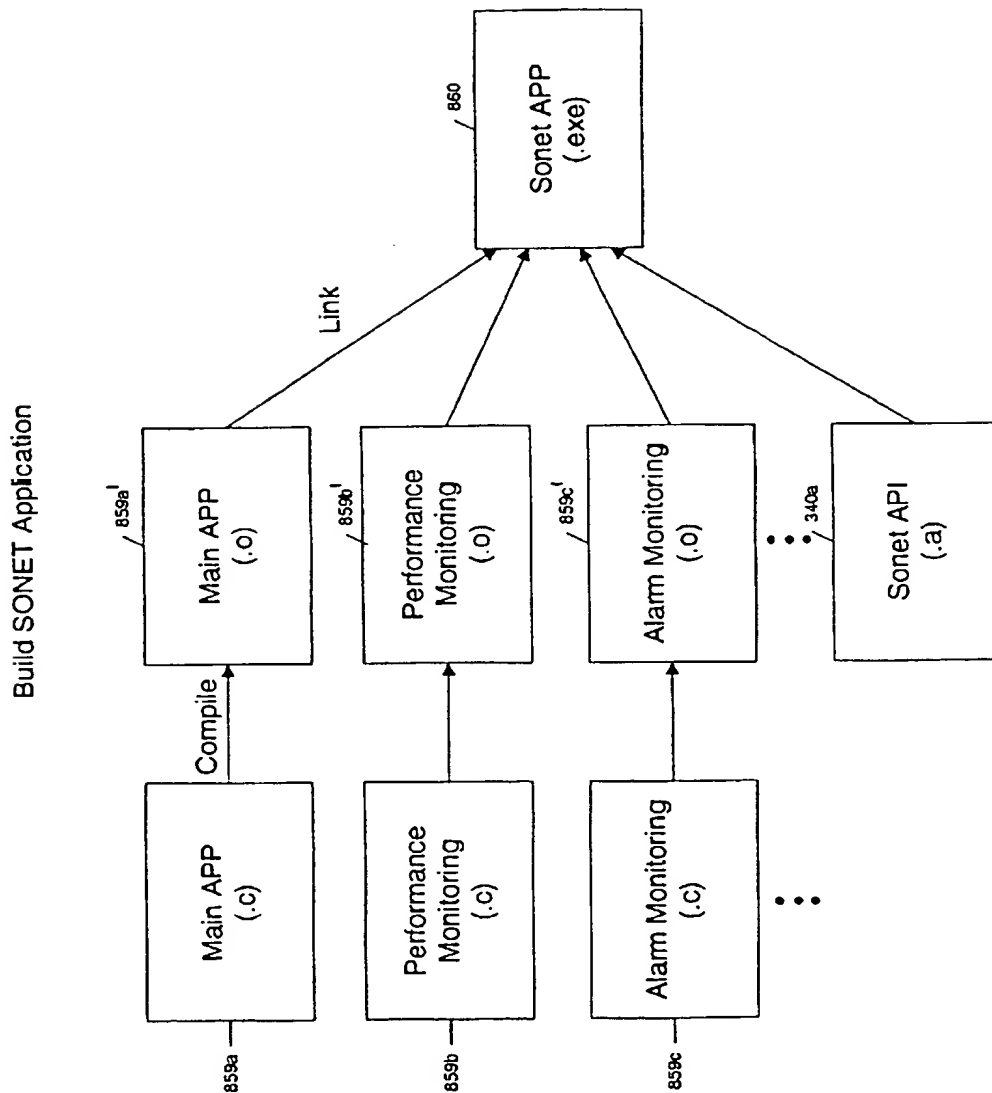


Fig. 3d



00T120"0084E960

Fig. 3e

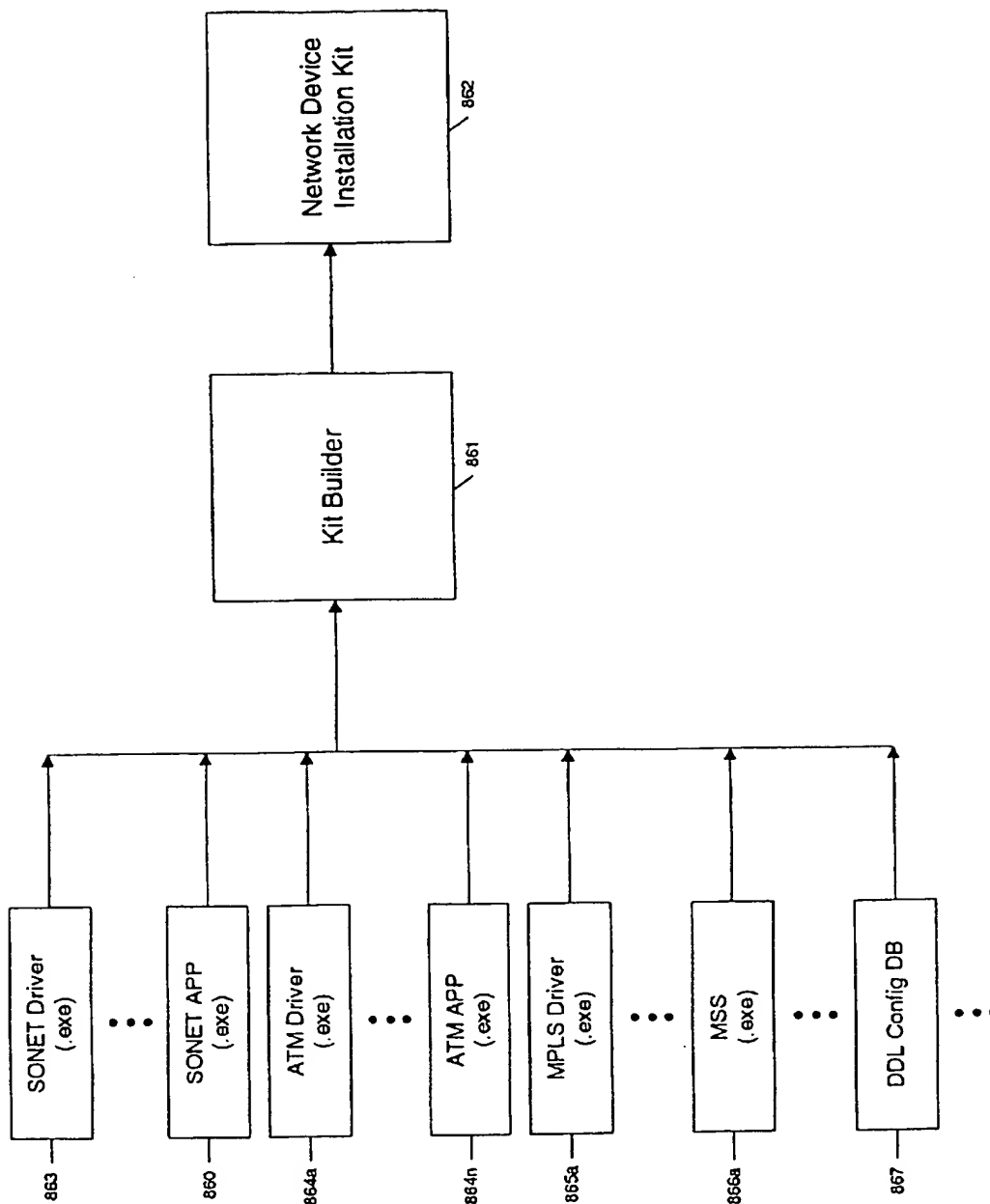


Fig. 34

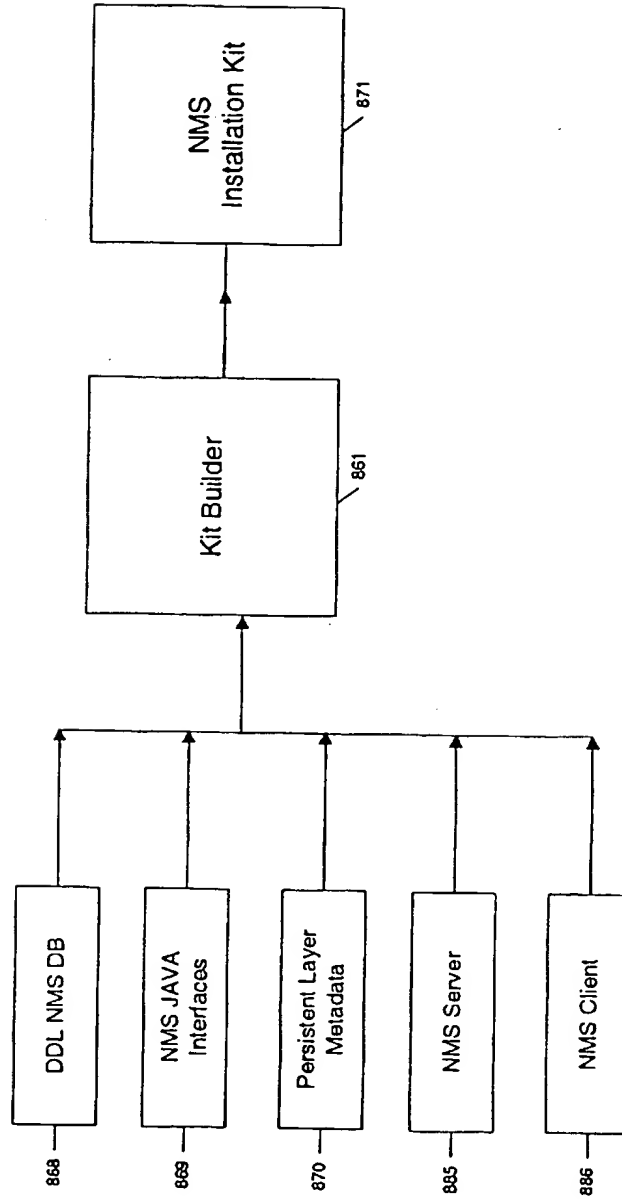
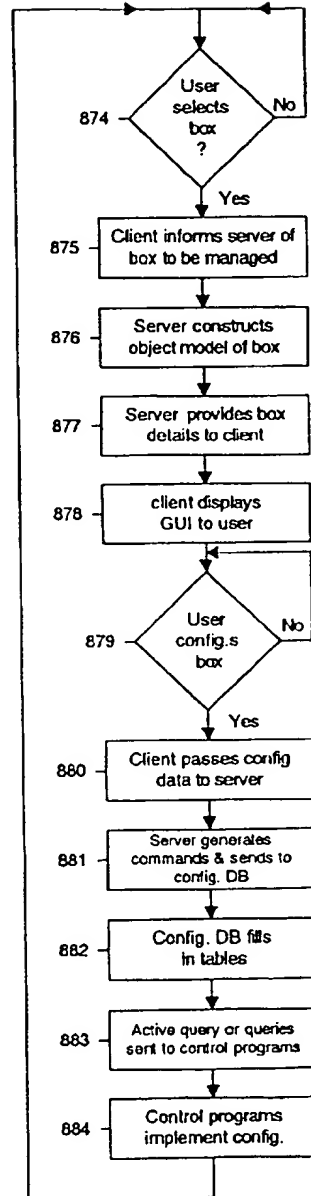


Fig. 3g



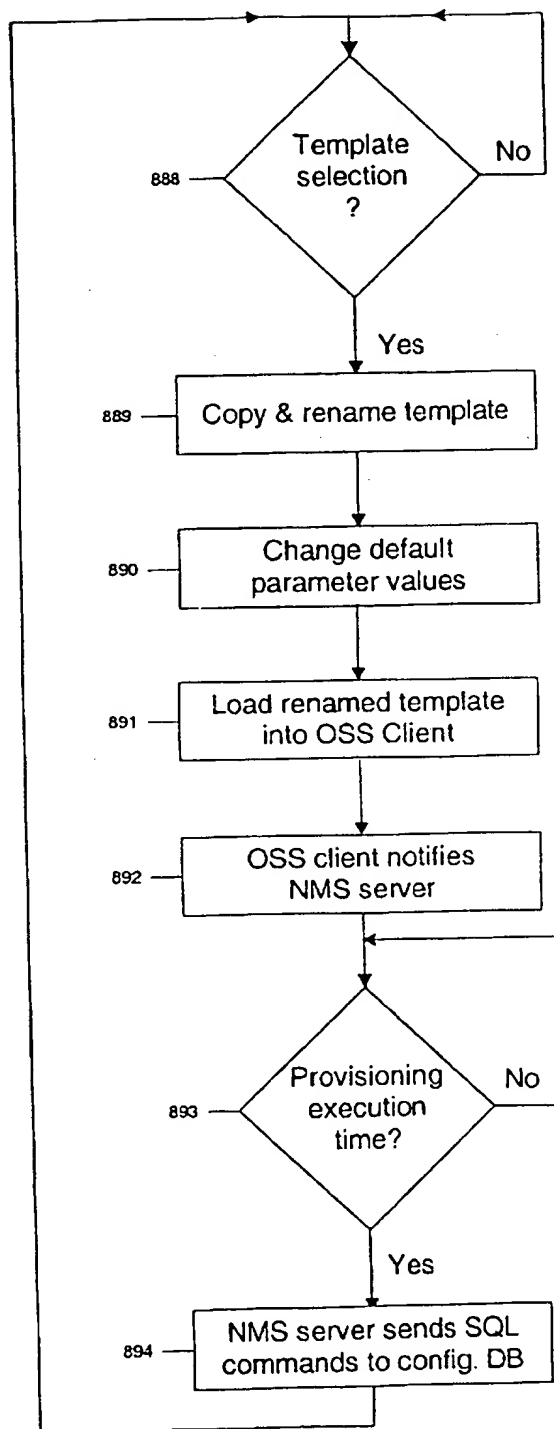
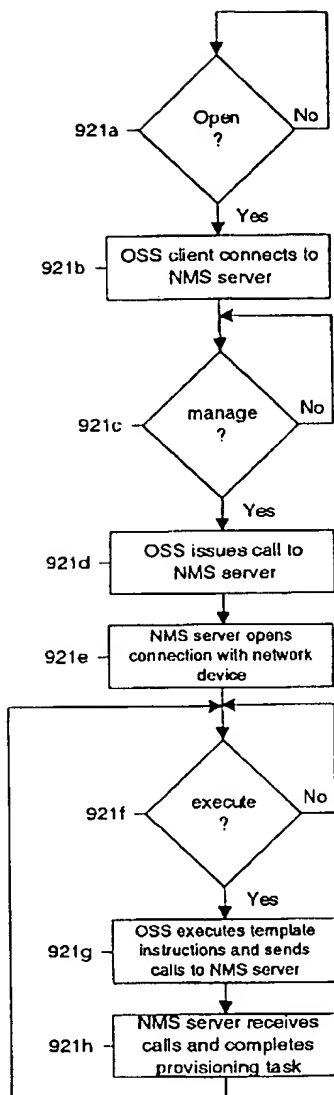


Fig. 3h

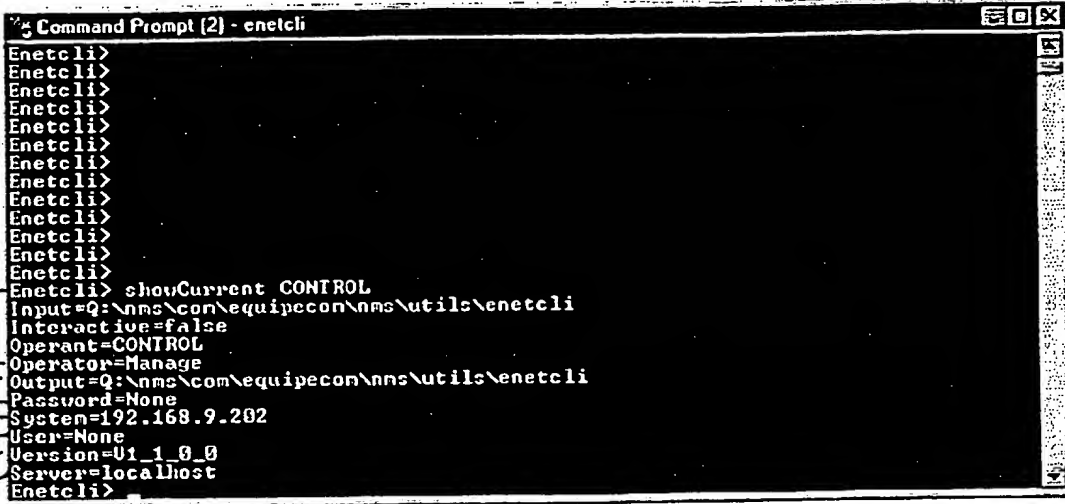
09637800-081400

Fig. 3j



00T80"008/E960

Fig. 3K



```
Command Prompt (2) - enetccli
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
Enetccli>
922- Enetccli> showCurrent CONTROL
Input=Q:\nms\com\equipecon\nms\utils\enetccli
Interactive=false
Operator=CONTROL
923d- Operator=Manage
923f- Output=Q:\nms\com\equipecon\nms\utils\enetccli
923c- Password=None
923e- System=192.168.9.202
923b- User=None
923g- Version=01_1_0_0
923h- Server=localhost
923a- Enetccli>
```

00T180-0087E960

Fig. 3L

← 924 BATCH

```

Operand=BATCH
Operator=Execute
Version=V1_1_0_0
924a—Task1=execute-SPATH
924b—Task2=execute-PVC
924c—Task3=execute-SPVC
924d—Task4=load-SPVC-spvc1
924e—Task5=execute-SPVC
924f—Task6=load-SPVC-spvc2
924e—Task7=excute-SPVC

```

```

924g - Task50=set-SPATH-PortID-3
924h - Task51=execute-SPATH
924i - Task52=set-SPATH-SlotID-2
924j - Task53=executc-SPATH

```

SECRET

09637800-081100

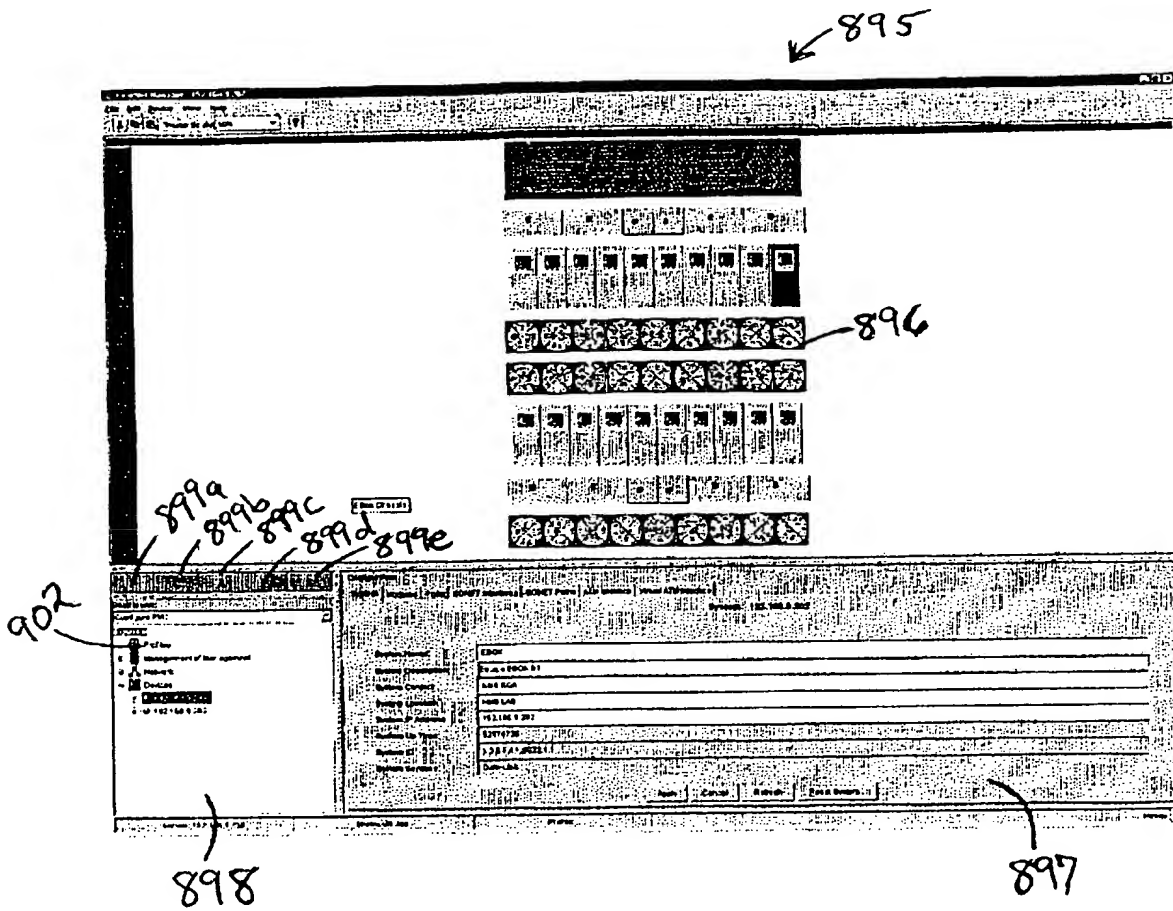


Fig. 4a

EvailNet Manager: Fault - Event Summary			
System: 192.132.65.160			
System	Event	Event Number	Description
1.1.55.6	Fan OverTemp	44	"Fan marginally functioning"
1.1.55.7	New Board Ins...	75	"New board inserted"
OK			

Fig. 4b

001180" 00829650

00780"0082E960

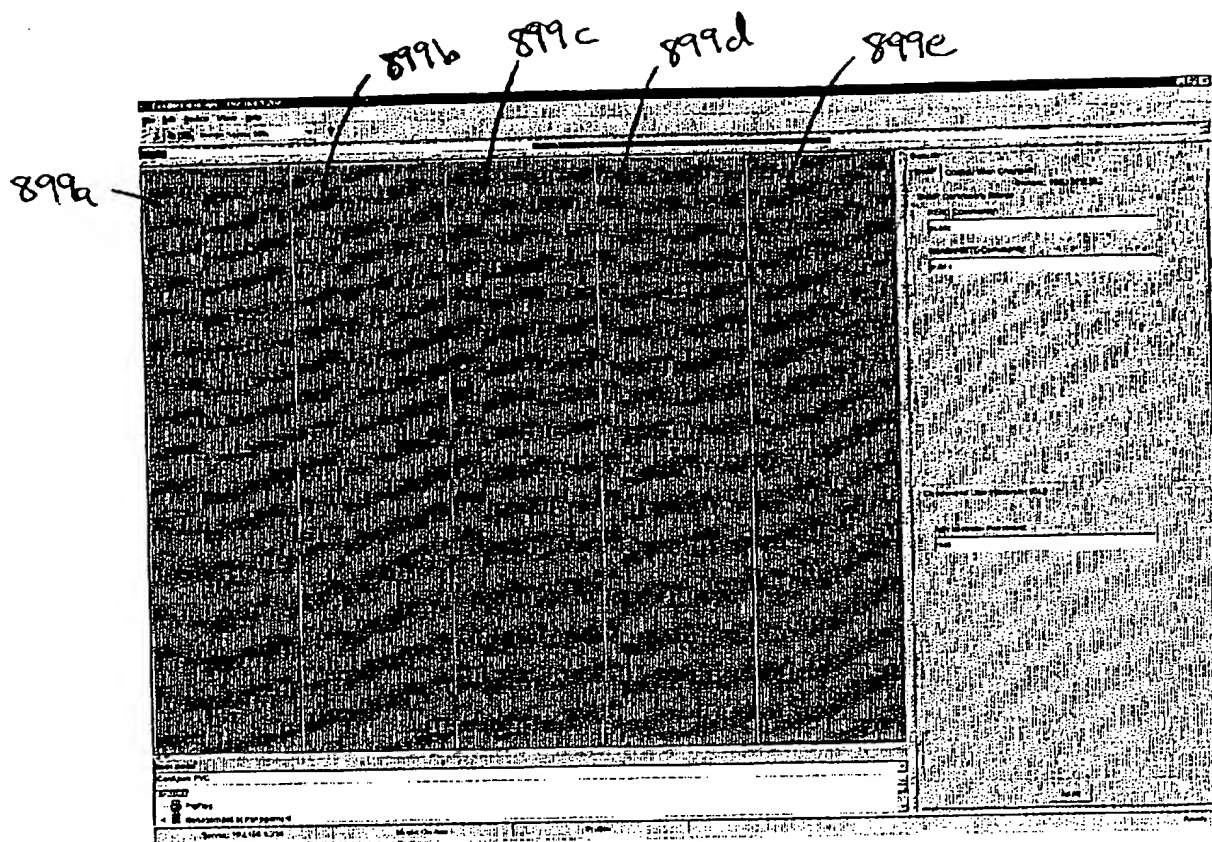


Fig. 4c

903

904

Name	Description	Security Level	Timeout	Primary Server	Secondary Server
Joe	Joe Whitehouse	Admin	15	192.168.1.32	192.168.1.37
Wame	Wayne Arena	Administrator	15	TeamServer1 192.168...	TeamServer2 192.168 1.32

905

906

Fig. 4d

007F80"0084E960

Fig. 4e

←907

General

Username: Kevin

Description: Kevin Snow user account

Group Name: Equipe

Group Level Access:

Password: *****

Confirm Password: *****

Policies

☒ User Cannot Change Password

☐ Account Disabled

☒ User Can Add Devices

User Session Timeout: 15 Minutes

Servers

Primary Server: 192.168.1.220

Primary Server Port: 6500

Secondary Server: 192.168.1.221

Secondary Server Port: 6503

Devices

Device	READ	READWRITE	Retry	Timeout
192.168.9.202	public	equipe	3	5
192.168.9.205	public	equipe	3	5
192.168.9.216	public	equipe	3	5

Add Delete

OK Cancel

908a

908e

908f

908d

908b

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001100 0087800 0087800

908g

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908r

908s

Fig. 48

General Policies Servers Devices

Username: Kevin

Description: Kevin Snow user account

Customer Name: Equipe

Group Level Access: [dropdown]

Password: *****

Confirm Password: *****

OK Cancel

General Policies Servers Devices

☐ User Cannot Change Password

☐ Account Disabled

☒ User Can Add Devices

User Session Timeout: 15 Minutes

OK Cancel

Fig. 49

00537800-08100

Fig. 4h

General Policies Servers **Servers**

Primary Server: 192.168.1.220

Primary Server Port: 6500

Secondary Server: 192.168.1.205

Secondary Server Port: 6503

OK Cancel

General Policies Servers **Devices**

Device	READ	READWRITE	Retry	Timeout	Trap Port
192.168.9.202	public	equipe	3	5	162
192.168.9.205	public	equipe	3	5	162
192.168.9.216	public	equipe	3	5	5012

Add Delete

OK Cancel

Fig. 4i

007780" 0087E960

909

910a

910b

910c

910d

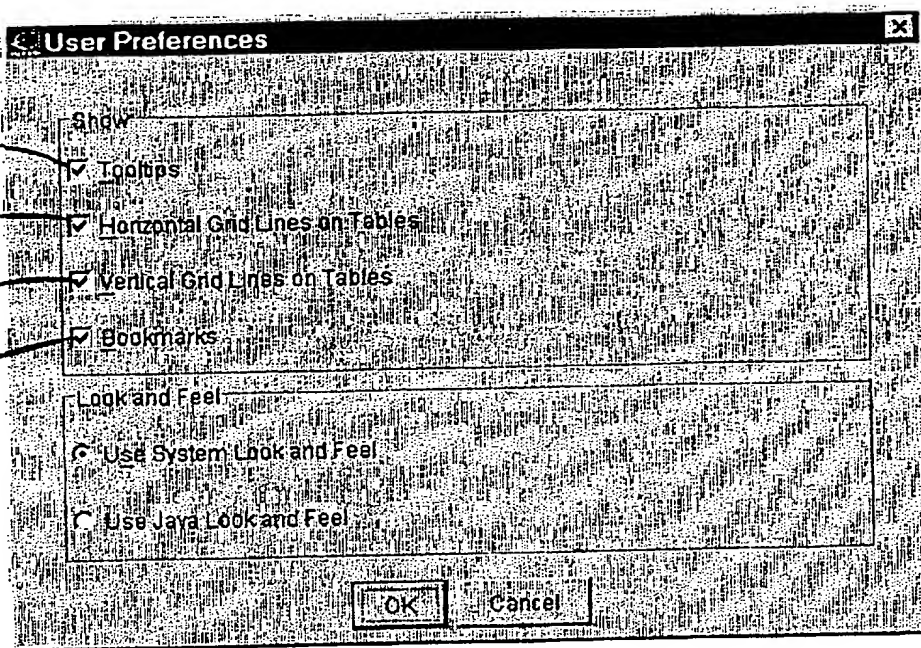
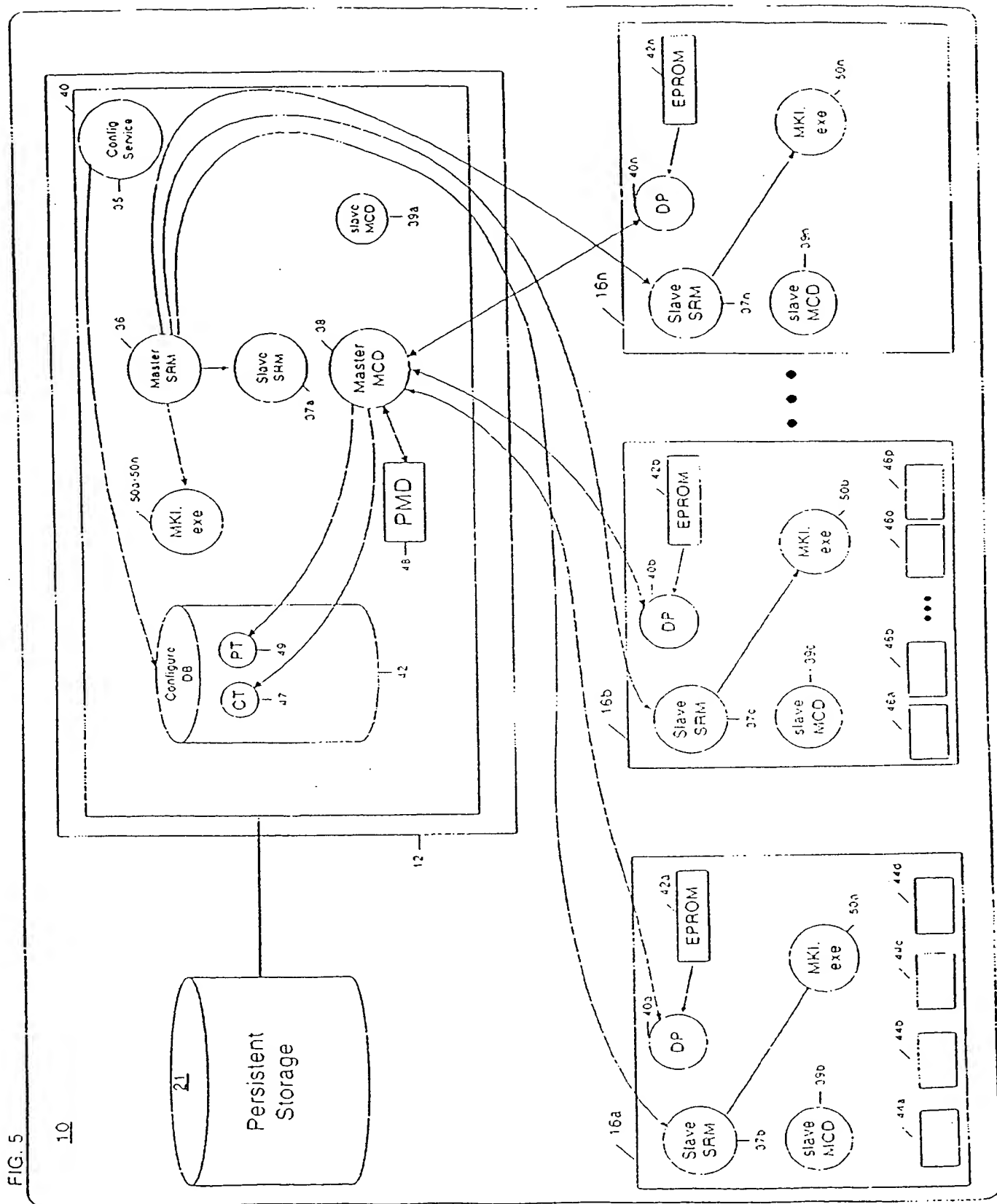


Fig. 4j

00537800-081100

FIG. 5



CARD TABLE

		PID	CWD TYPE	VERSION NO.	SLOT NO.	...
16 a	500	0XF002	3	1		
16 b	501	0XF002	4	2		
	:	:	:	:	:	:
	:	:	:	:	:	:
16 e	505	0X6002	1	5		
	:	:	:	:	:	:
	:	:	:	:	:	:
16 n	513	0XF002	1	12		
	:	:	:	:	:	:
	:	:	:	:	:	:

47

FIG. 6

PORT TABLE

49

PID	PORT TYPE	VERSION NO.	SLOT NO.	...
44a	1500	00620	1	1
44b	1501	00620	1	1
44c	1502	00620	1	1
44d	1503	00620	1	1
46a	1504	00820		
:	:	:	:	:
:	:	:	:	:
1600	00620	1	8	
:	:	:	:	:
:	:	:	:	:

FIG. 7

FIG. 8

10

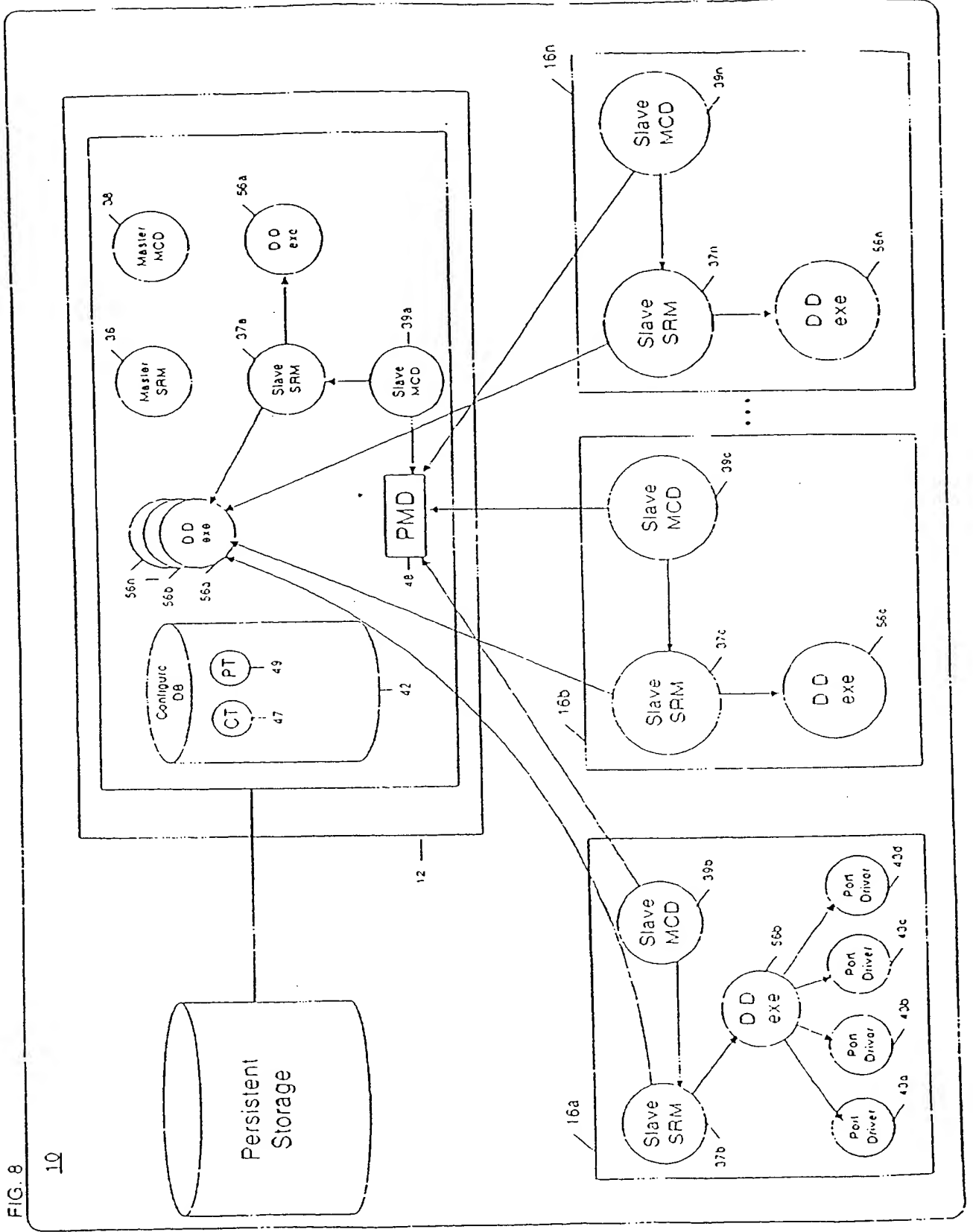


FIG. 9a

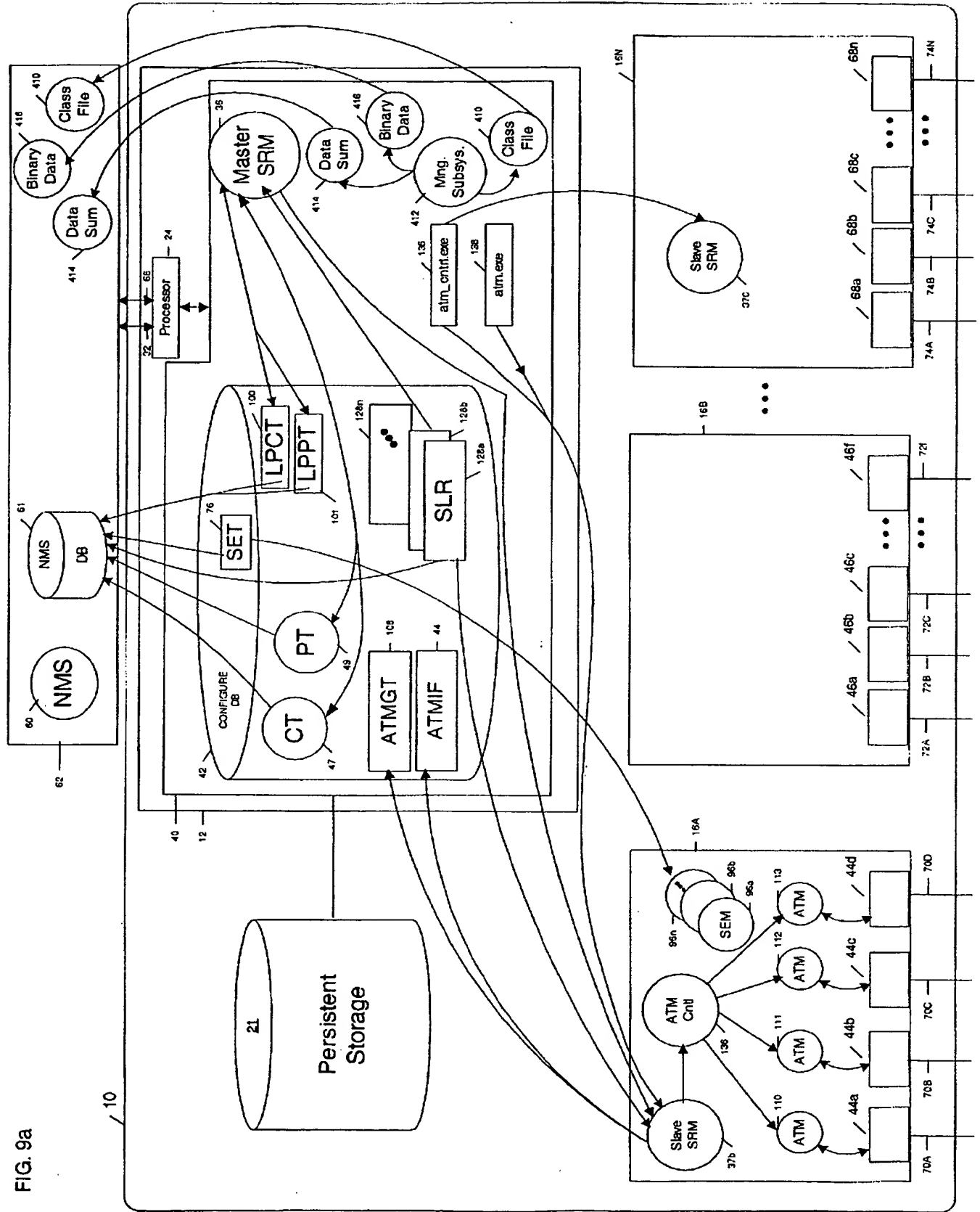


Fig. 9b

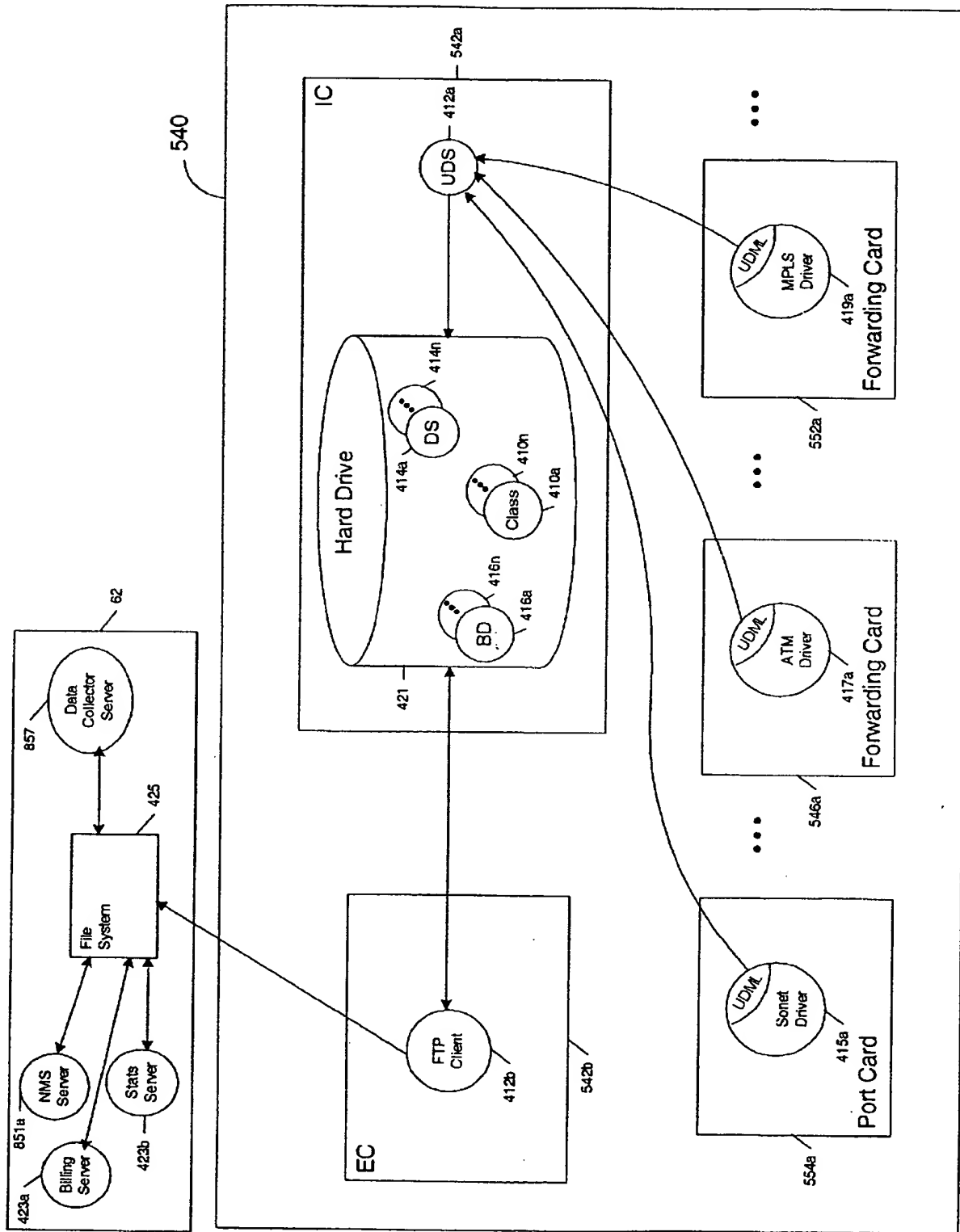


FIG. 10

Service Endpoint Table 76

	Service Endpoint #	Port PID
78	1	1500
80	2	1501
82	3	1501
84	4	1501
86	5	1502
88	6	1502
90	7	1503
92	8	1503
94	9	1503
168	10	1502
	⋮	⋮

FIG. 11a

Logical to Physical Card Table 100

	98 LID	102 Primary PID	104 Back-up PID
106	30	500	513
109	31	501	513
	⋮	⋮	⋮

FIG. 11b

Logical to Physical Port Table 101

	98 LID	102 Primary PID	104 Back-up PID
107	40	1500	1600
	⋮	⋮	⋮

FIG. 12

ATM Group Table 108

Group #	Card LID	...
1	30	
2	30	
3	30	
4	30	

FIG. 13

ATM Interface Table 114

ATM IF	ATM Group	SE	...
1	1	1	
2	1	1	
3	1	1	
4	2	2	
5	2	3	
6	2	4	
⋮	⋮	⋮	⋮
12	3	10	
⋮	⋮	⋮	⋮

FIG. 14

Software Load Record 128a

130	Control Shim	LID	132
134	atm-cntrl.exe	30	

FIG. 15

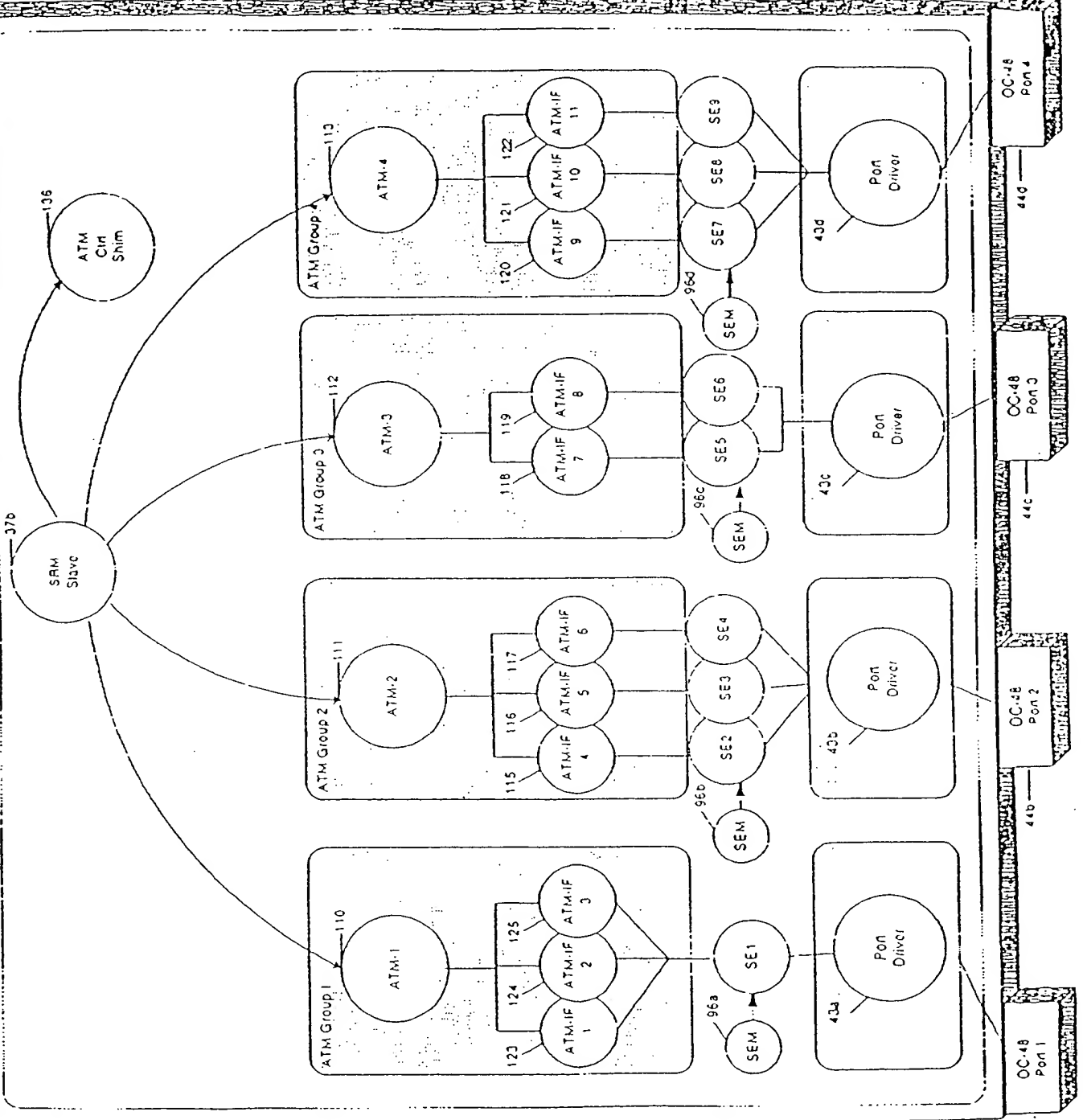


FIG. 16a

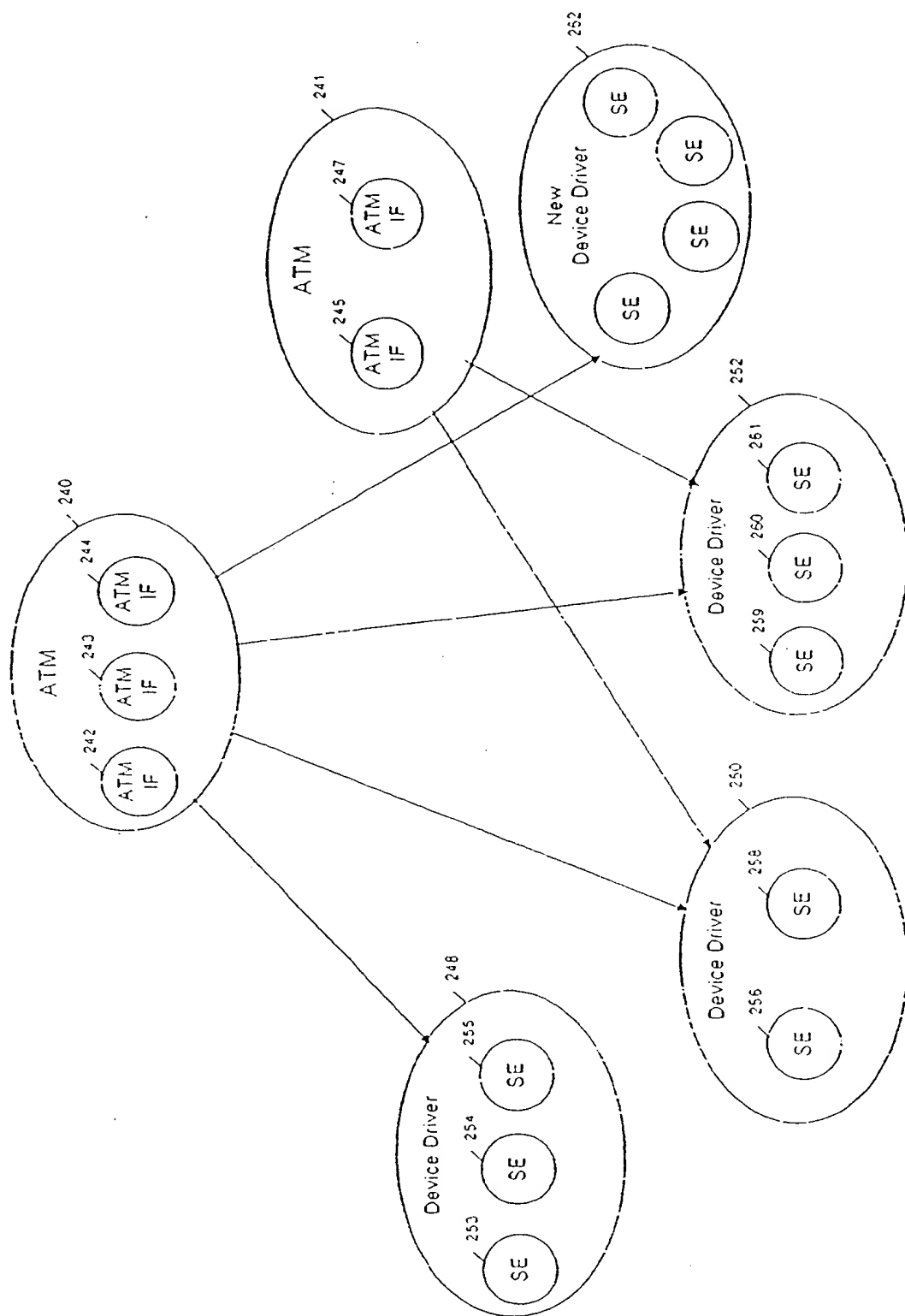


FIG. 16b

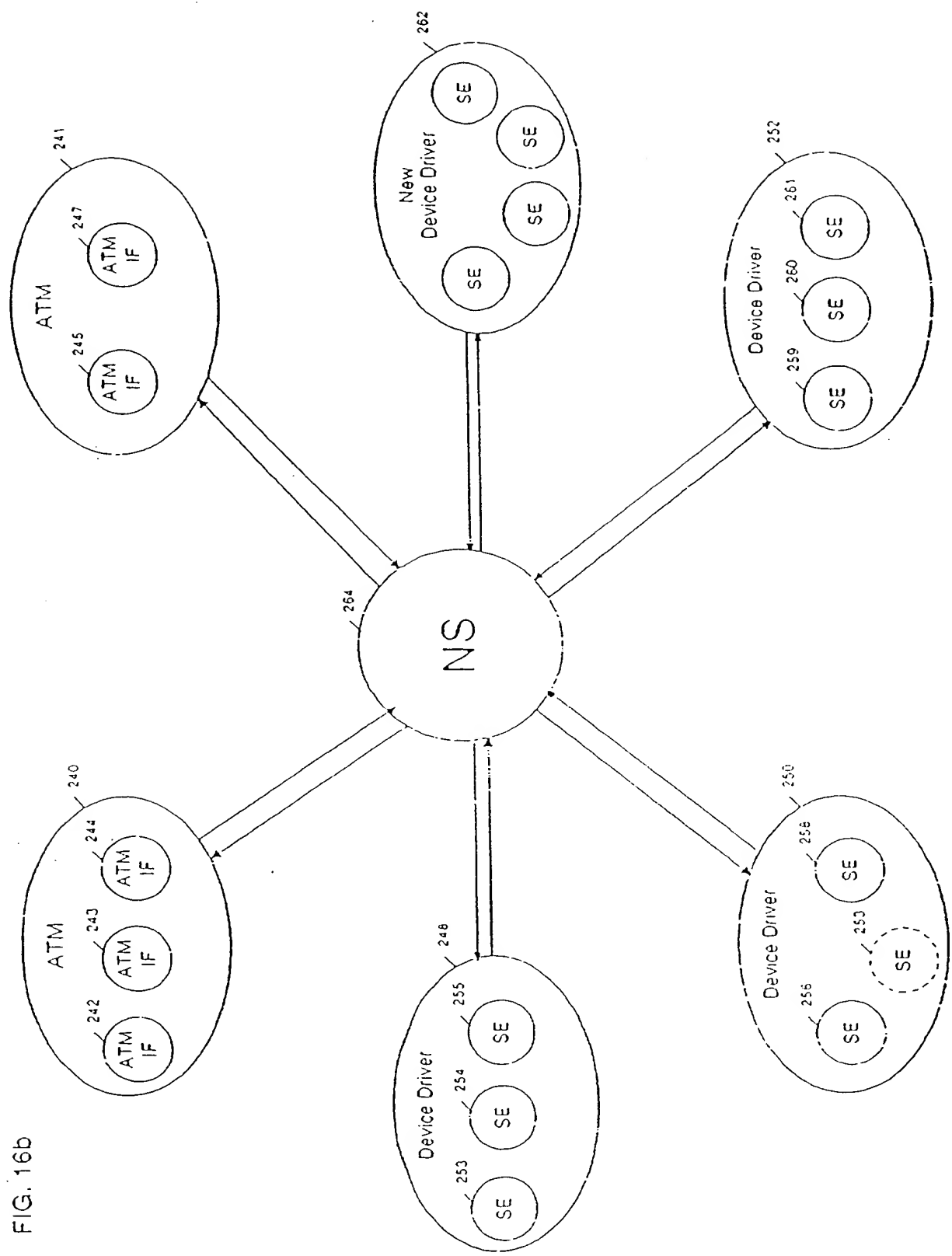
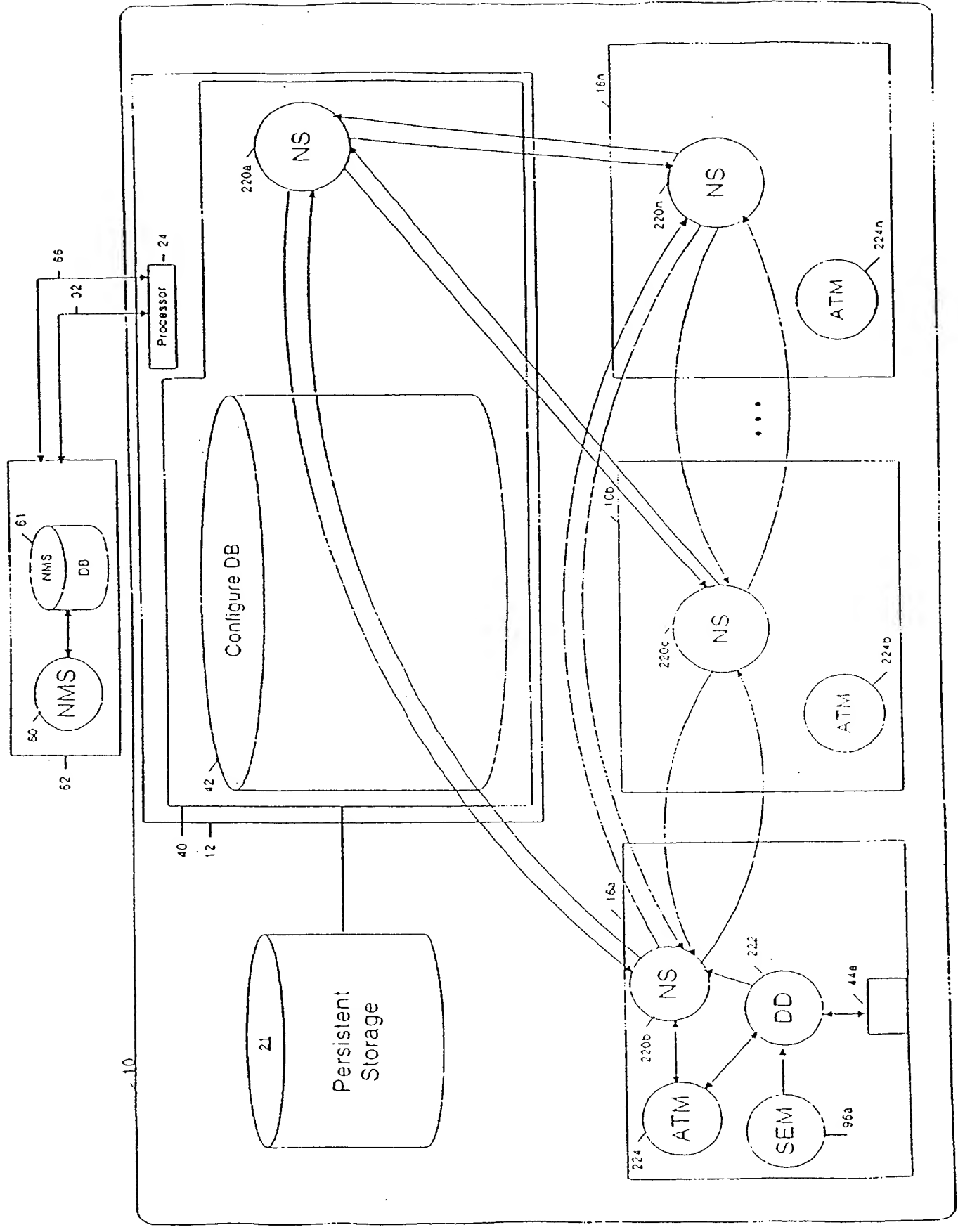


FIG. 16c





SECRET

FIG. 17

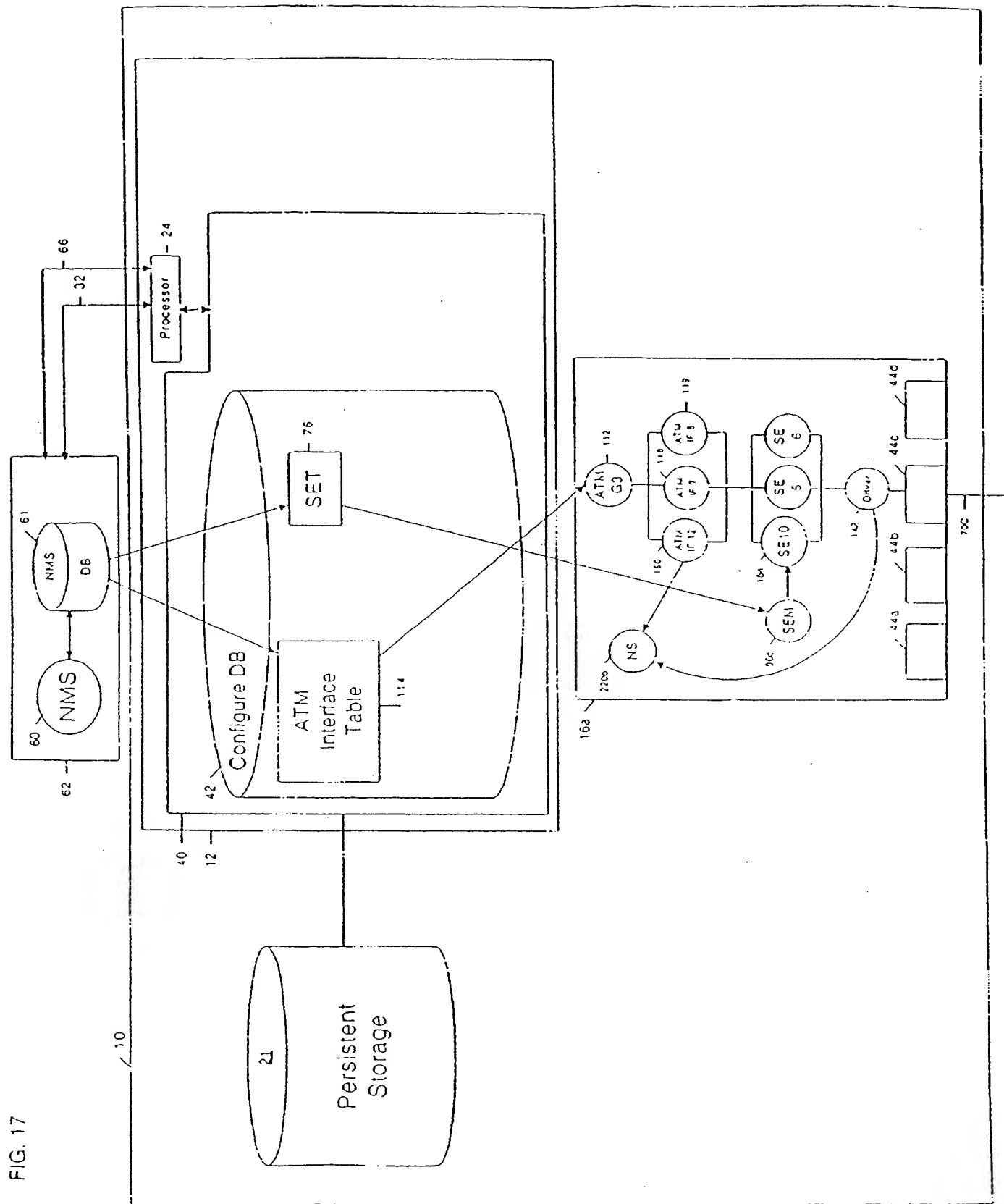


FIG. 18

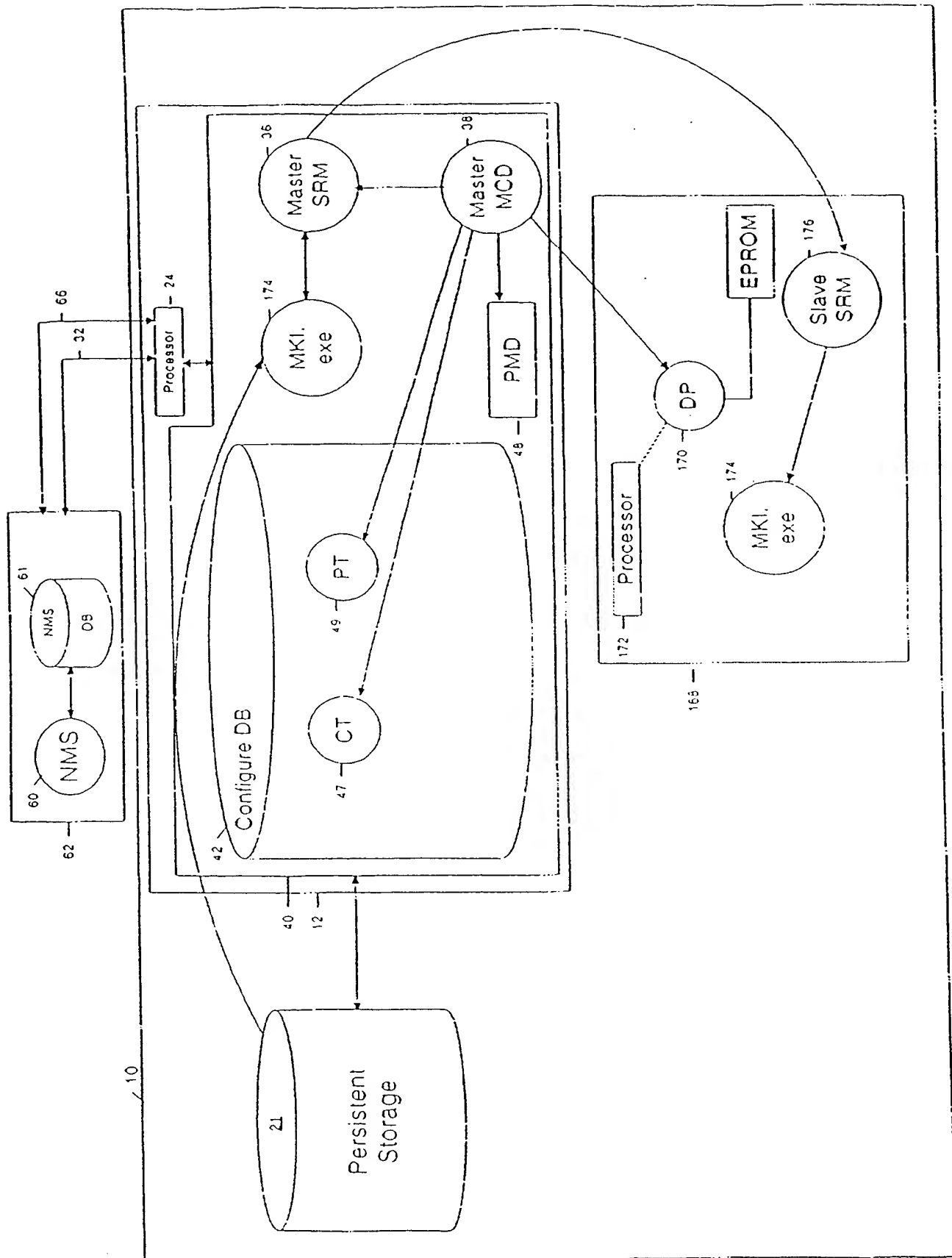


FIG. 19

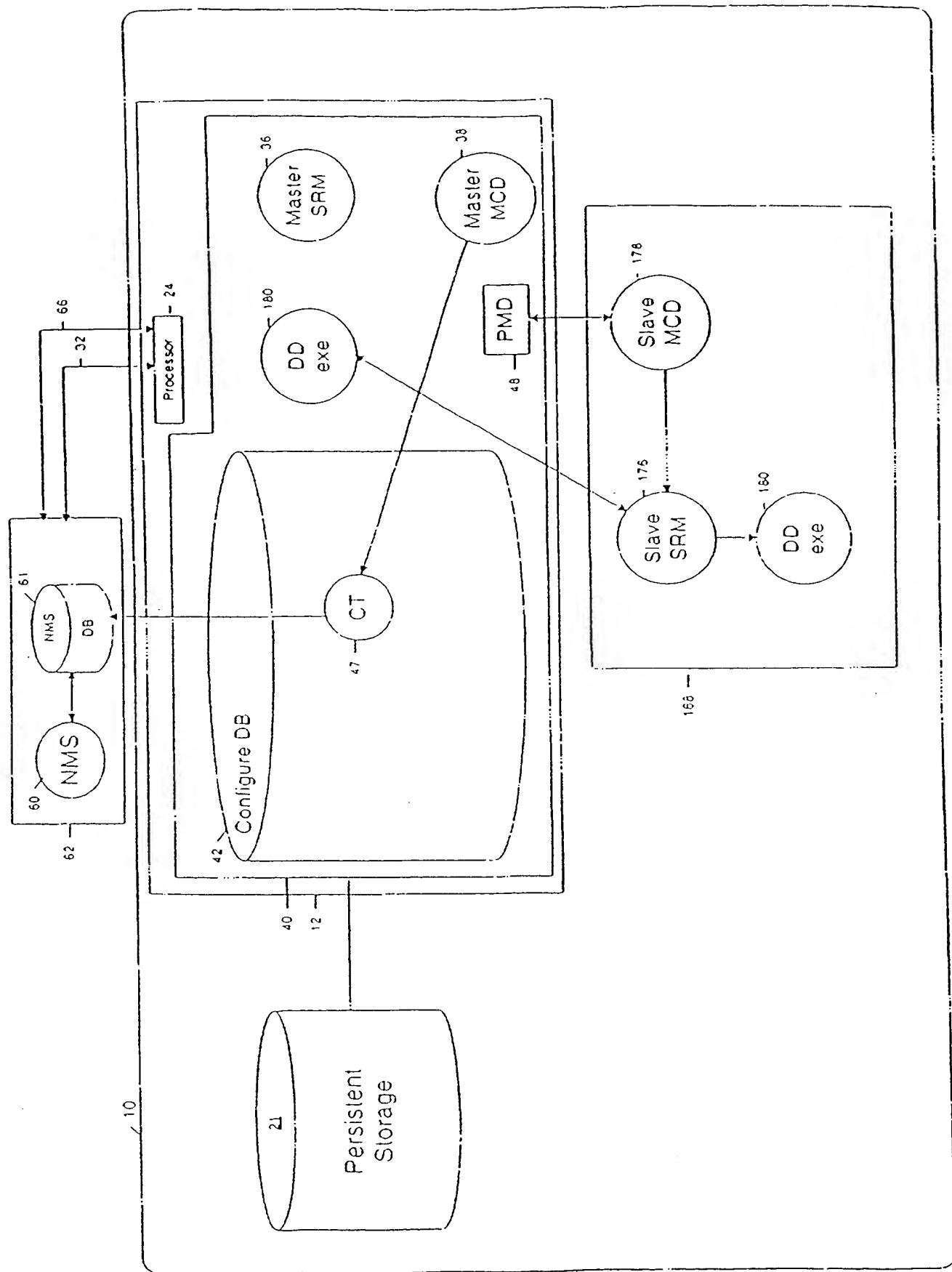


FIG. 20

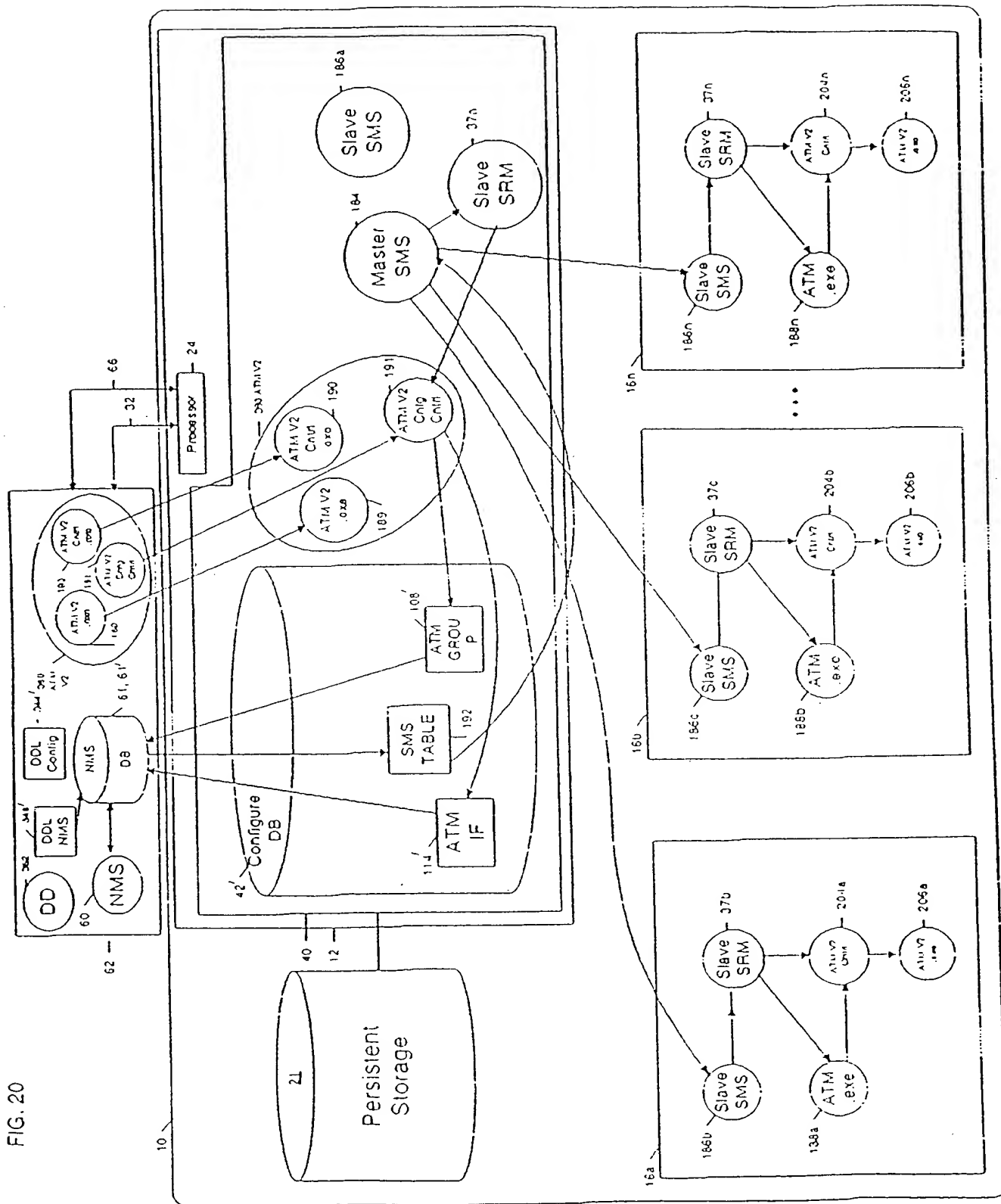


FIG. 21

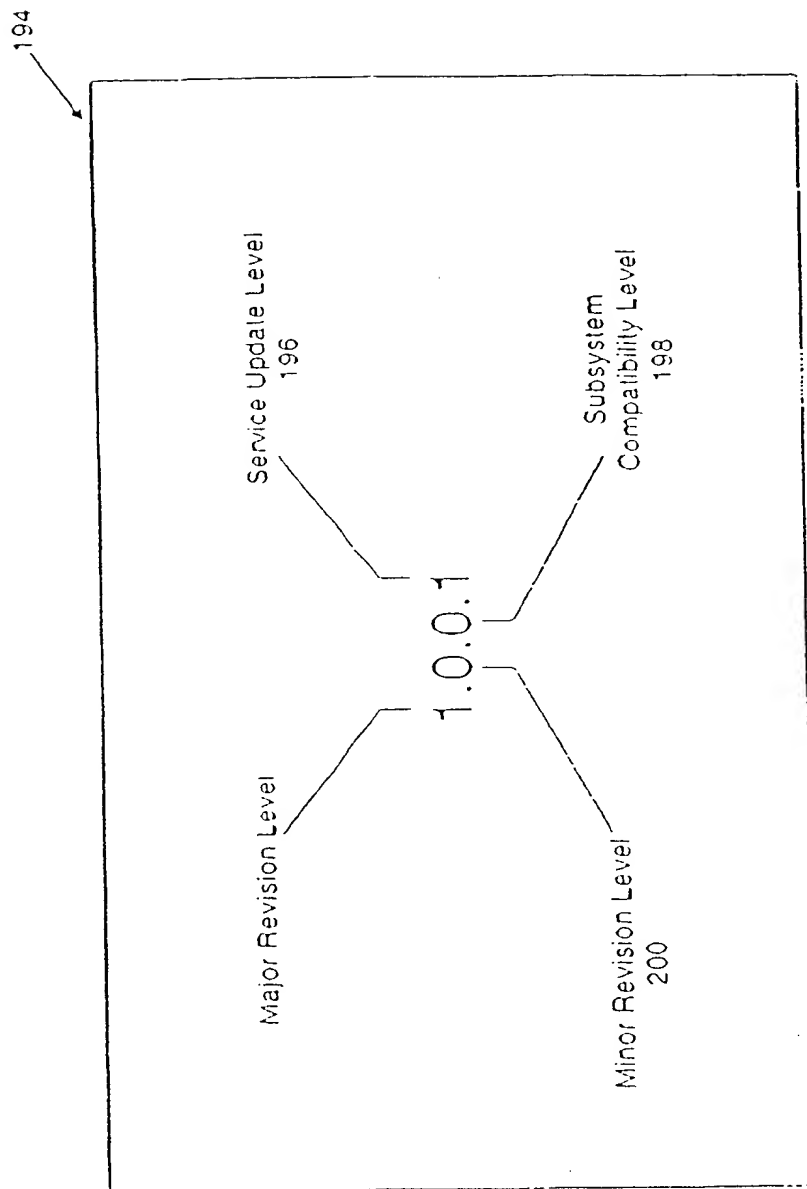


FIG. 22

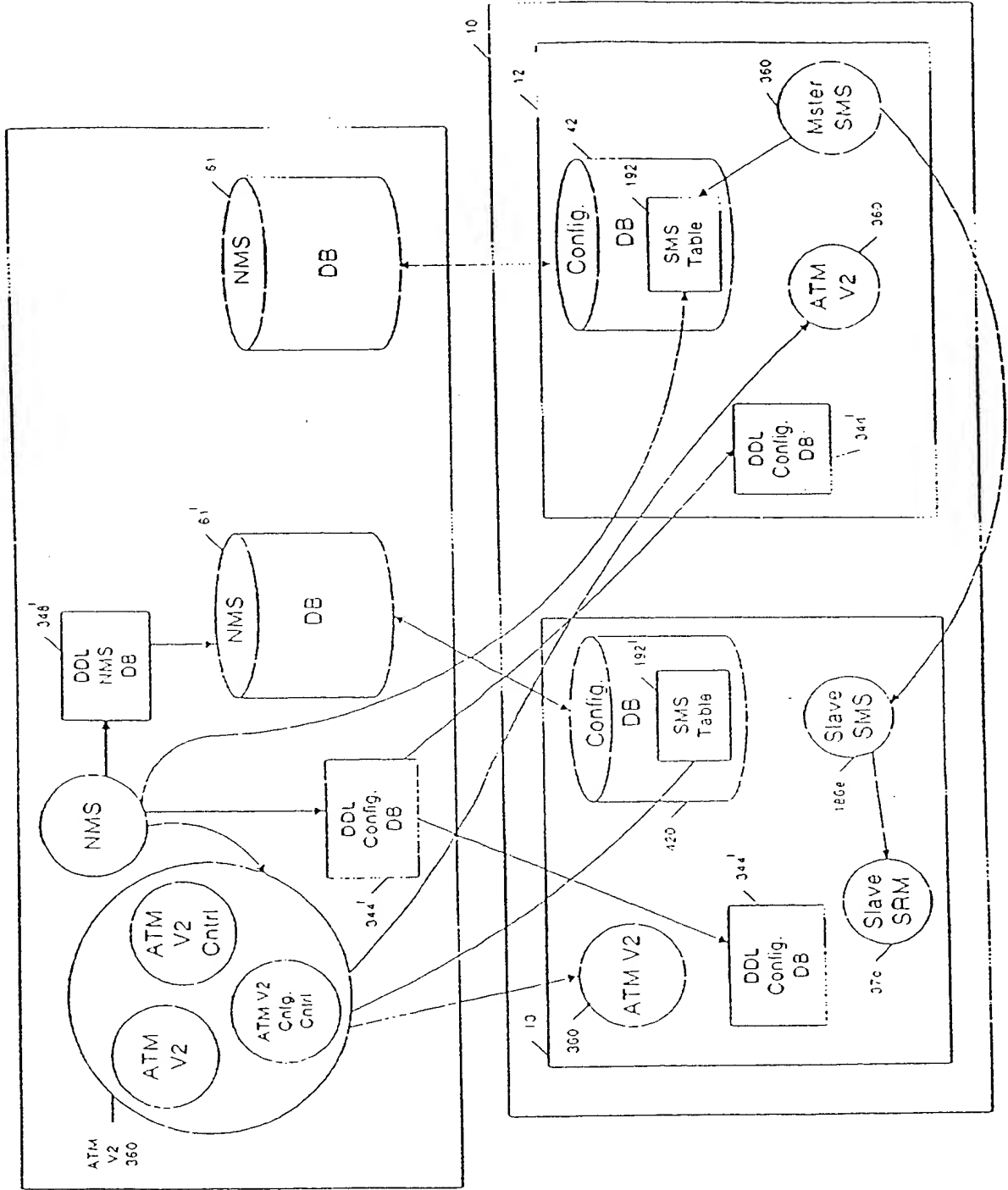


FIG. 24

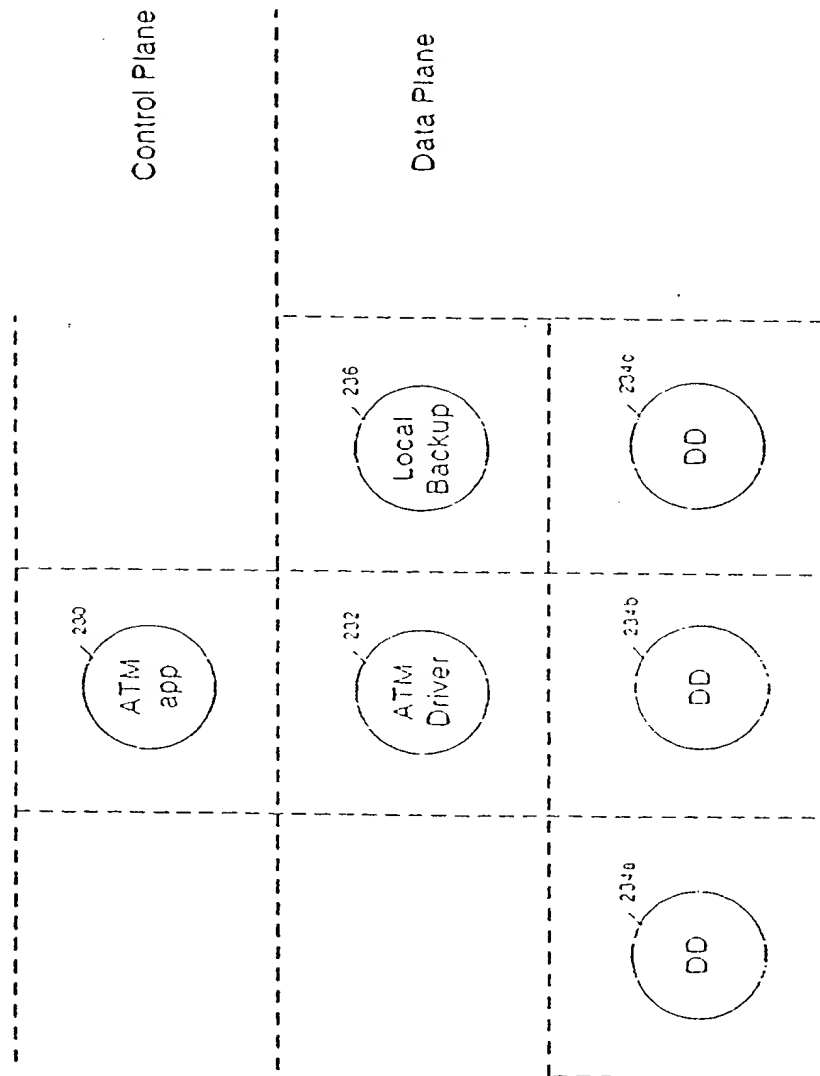


FIG. 25

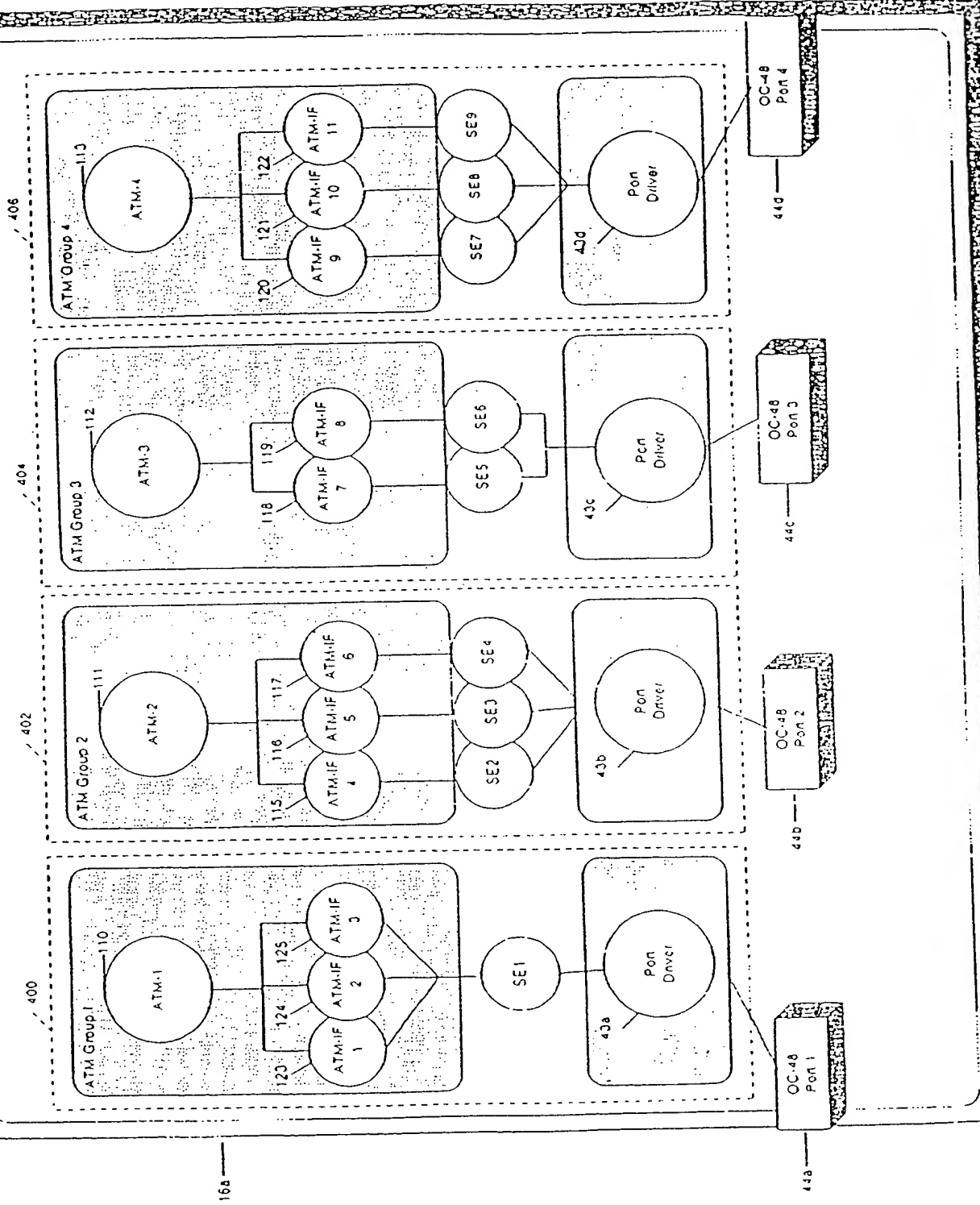


FIG. 26

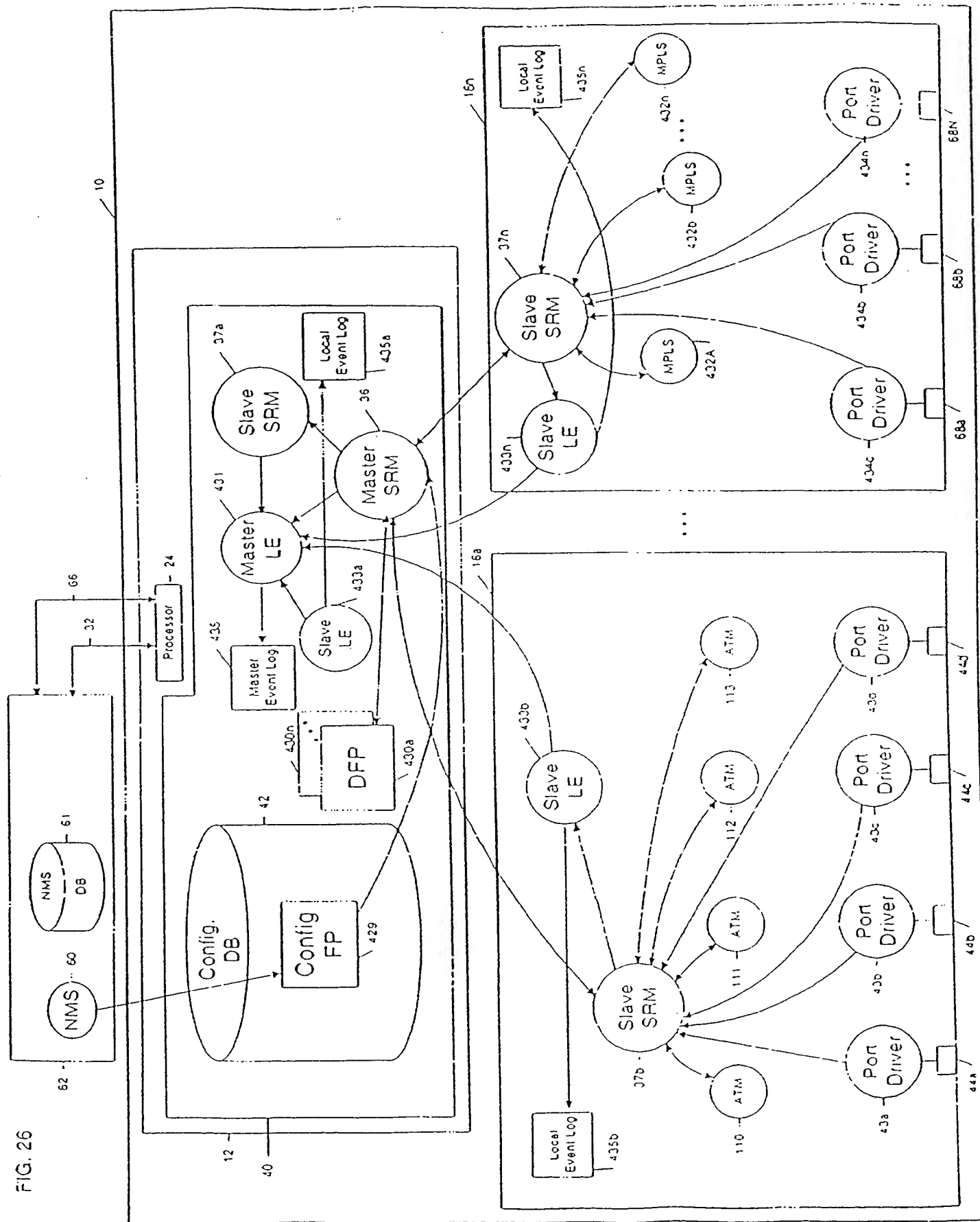


FIG. 27

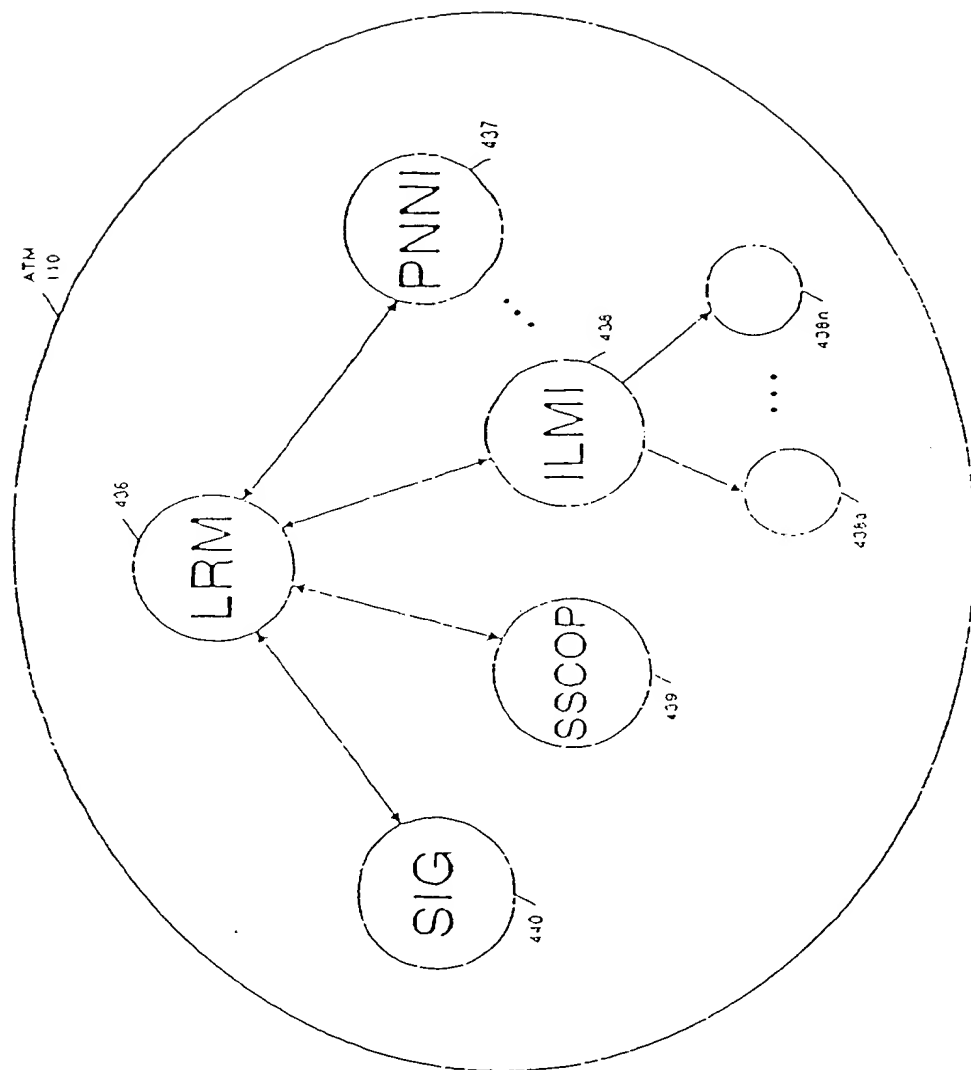


FIG. 28

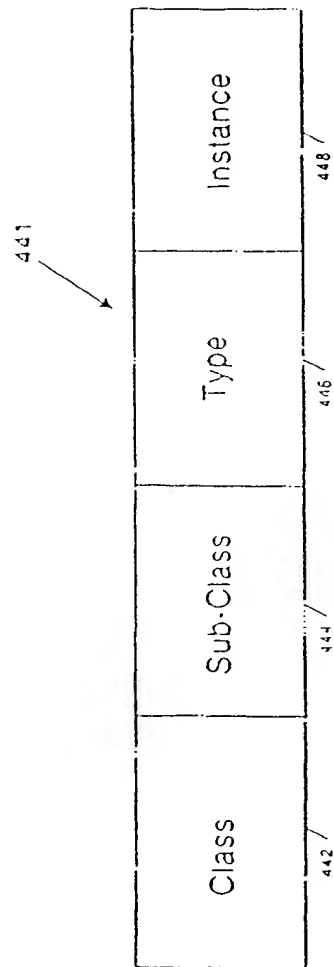
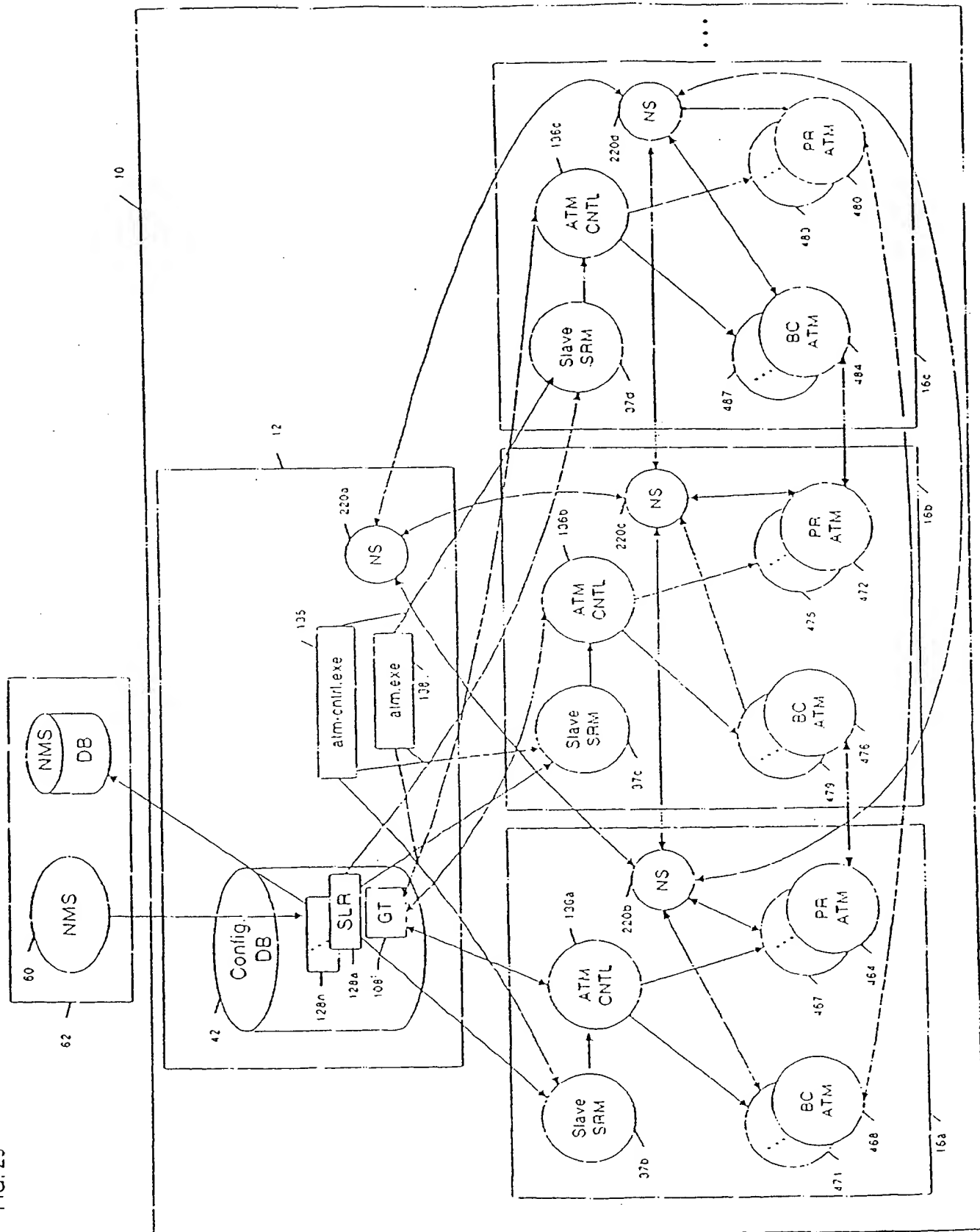


FIG. 29



Group Table 108'

	Group #	Primary Card LID	Backup Card LID	...
450	1	30	31	
451	2	30	31	
452	3	30	31	
453	4	30	31	
454	5	31	32	
455	6	31	32	
456	7	31	32	
457	8	31	32	
458	9	32	30	
459	10	32	30	
460	11	32	30	
461	12	32	30	
	⋮	⋮	⋮	⋮

[illegible]

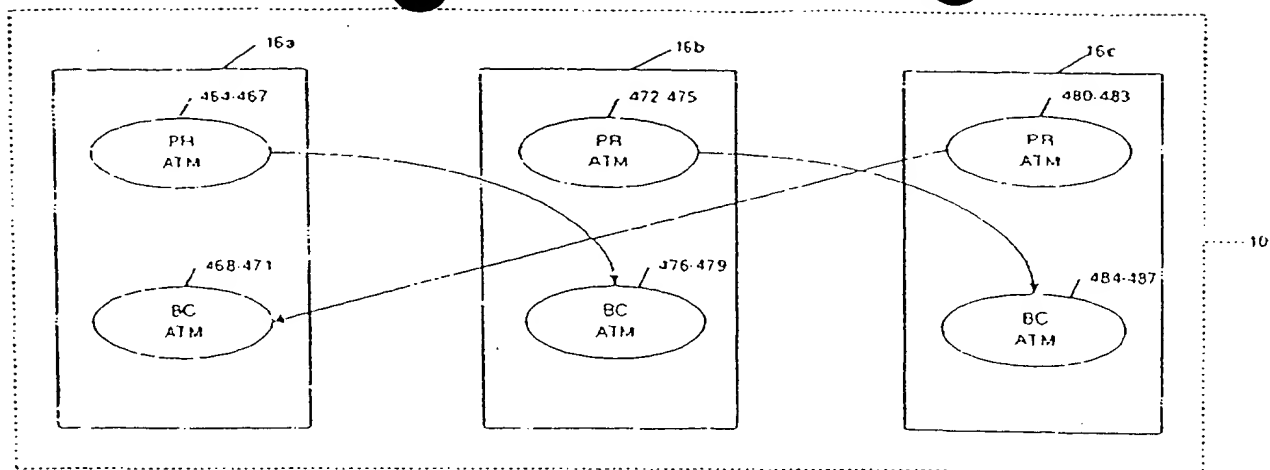


Fig. 31b

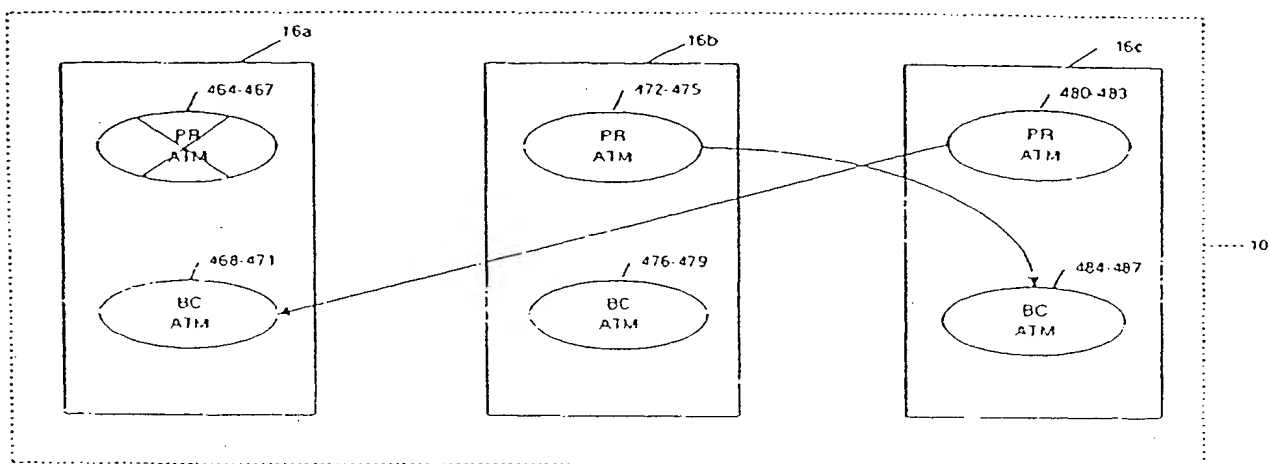


Fig. 31c

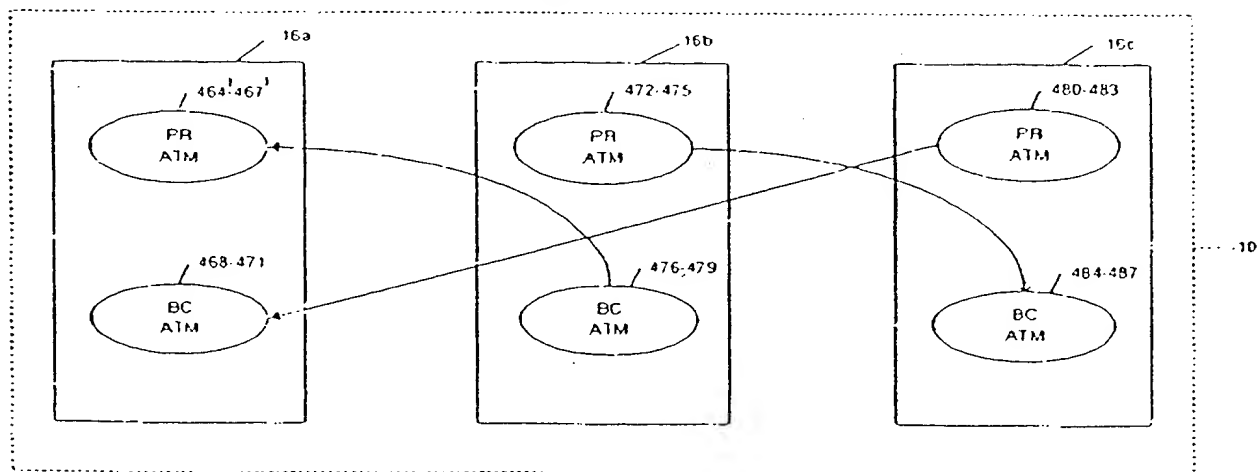


Fig. 32a

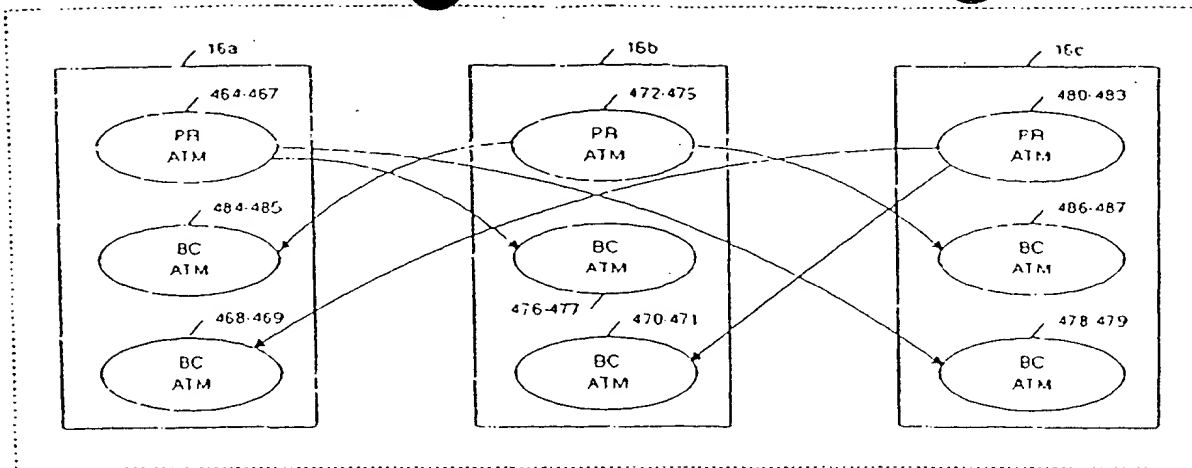


Fig. 32b

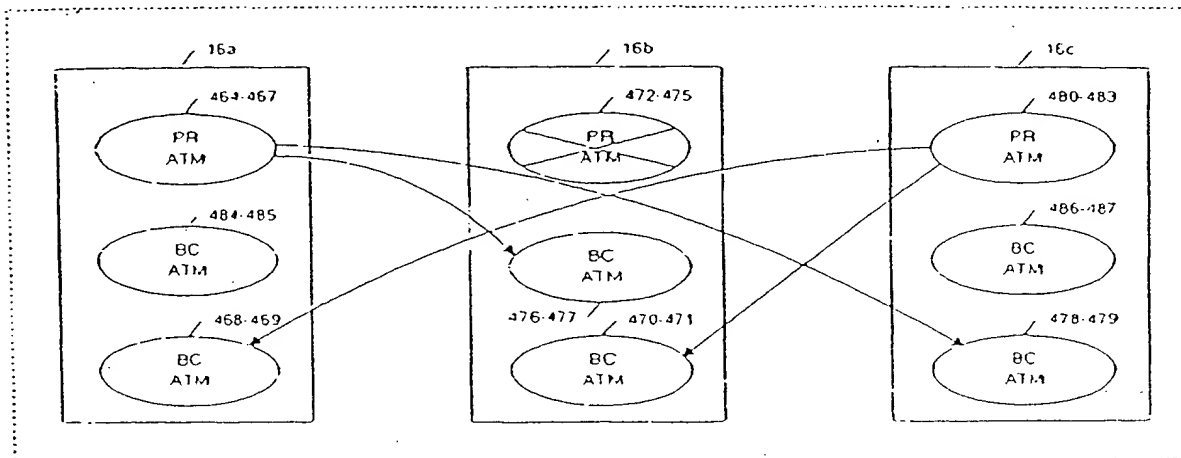


Fig. 32c

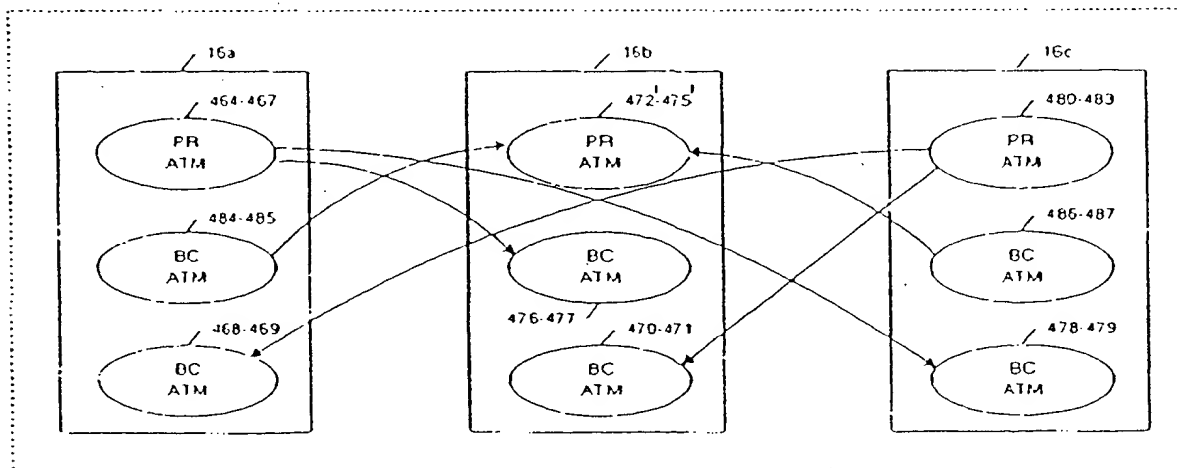


FIG. 33a

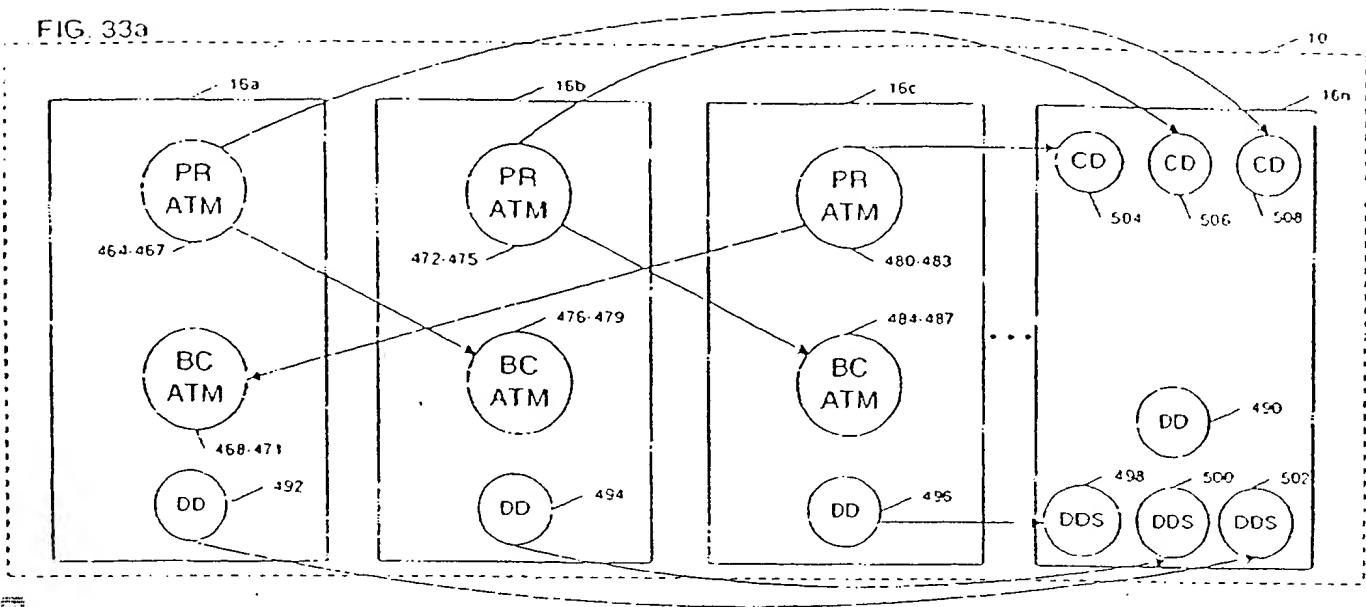


FIG. 33b

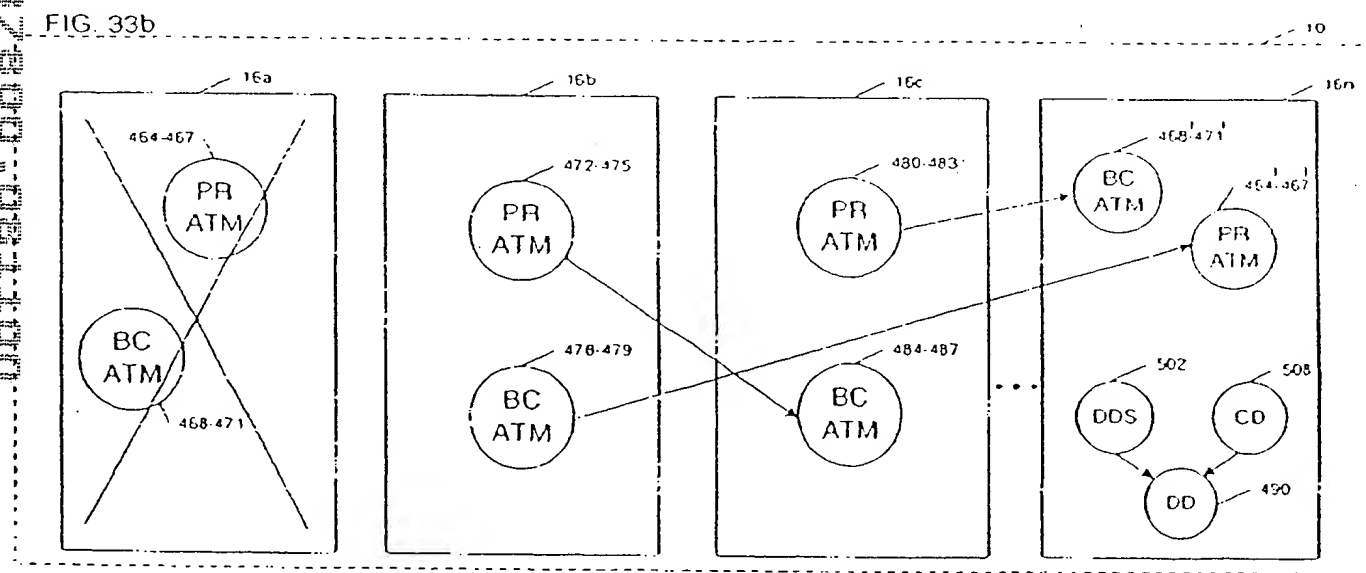


FIG. 33c

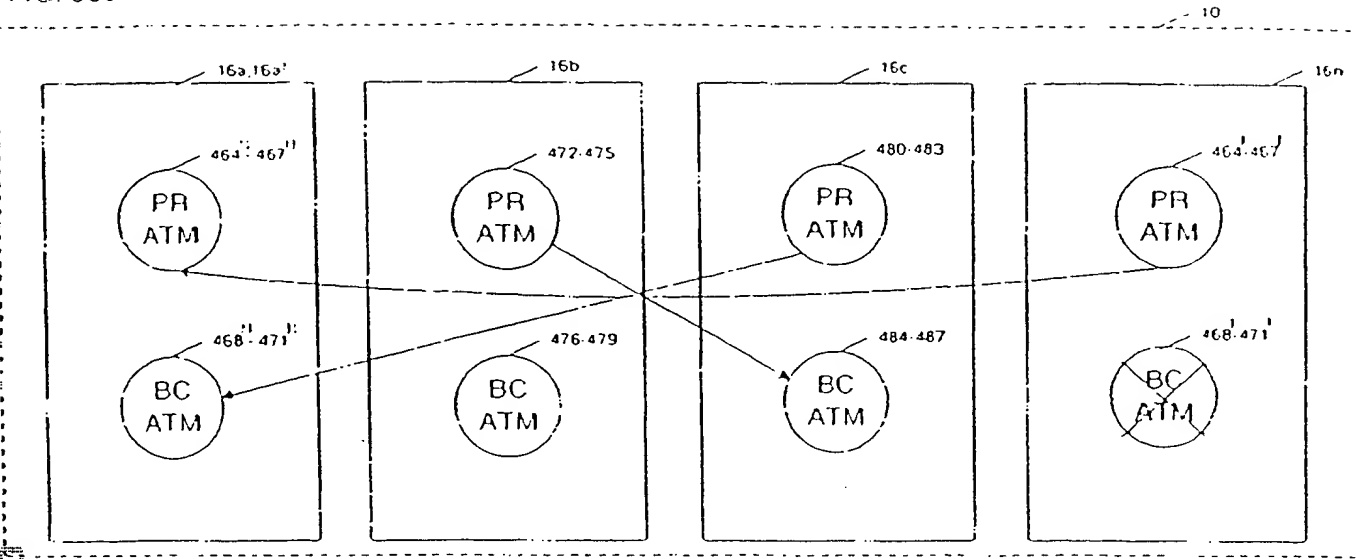


FIG. 33d

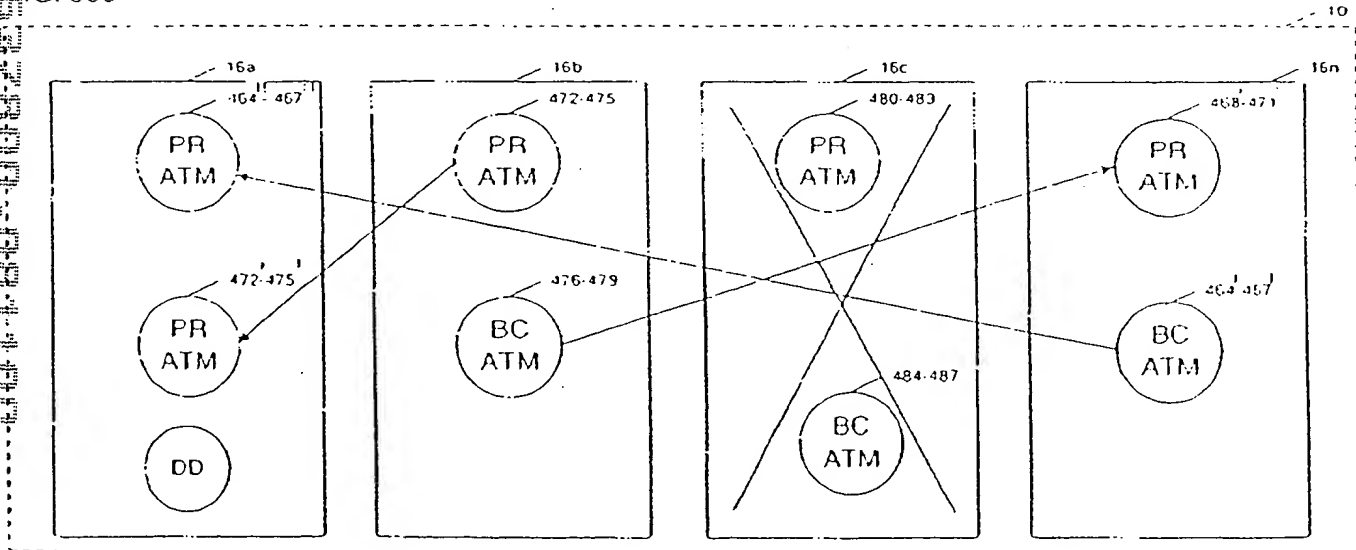


Fig. 34a

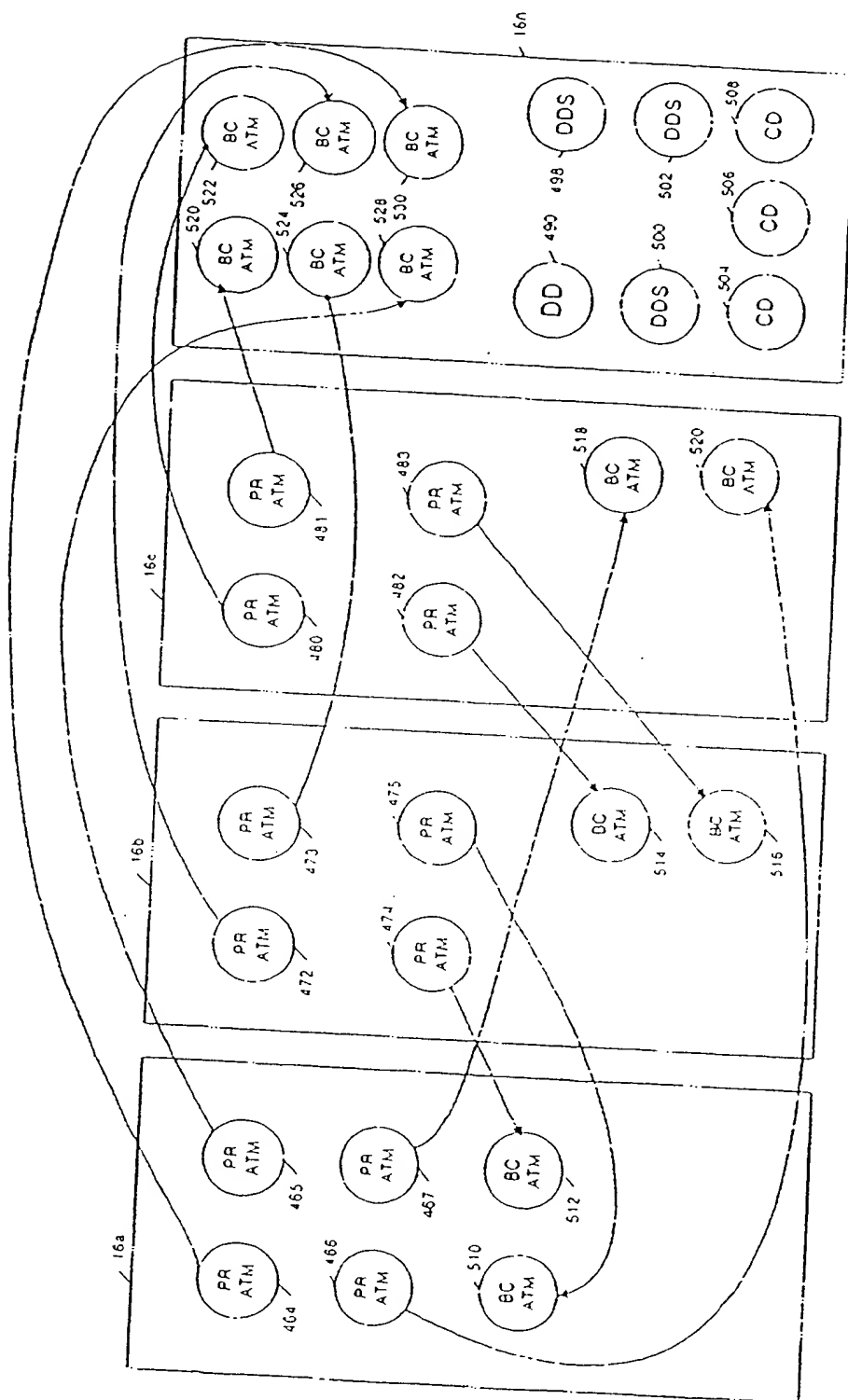


FIG. 34b

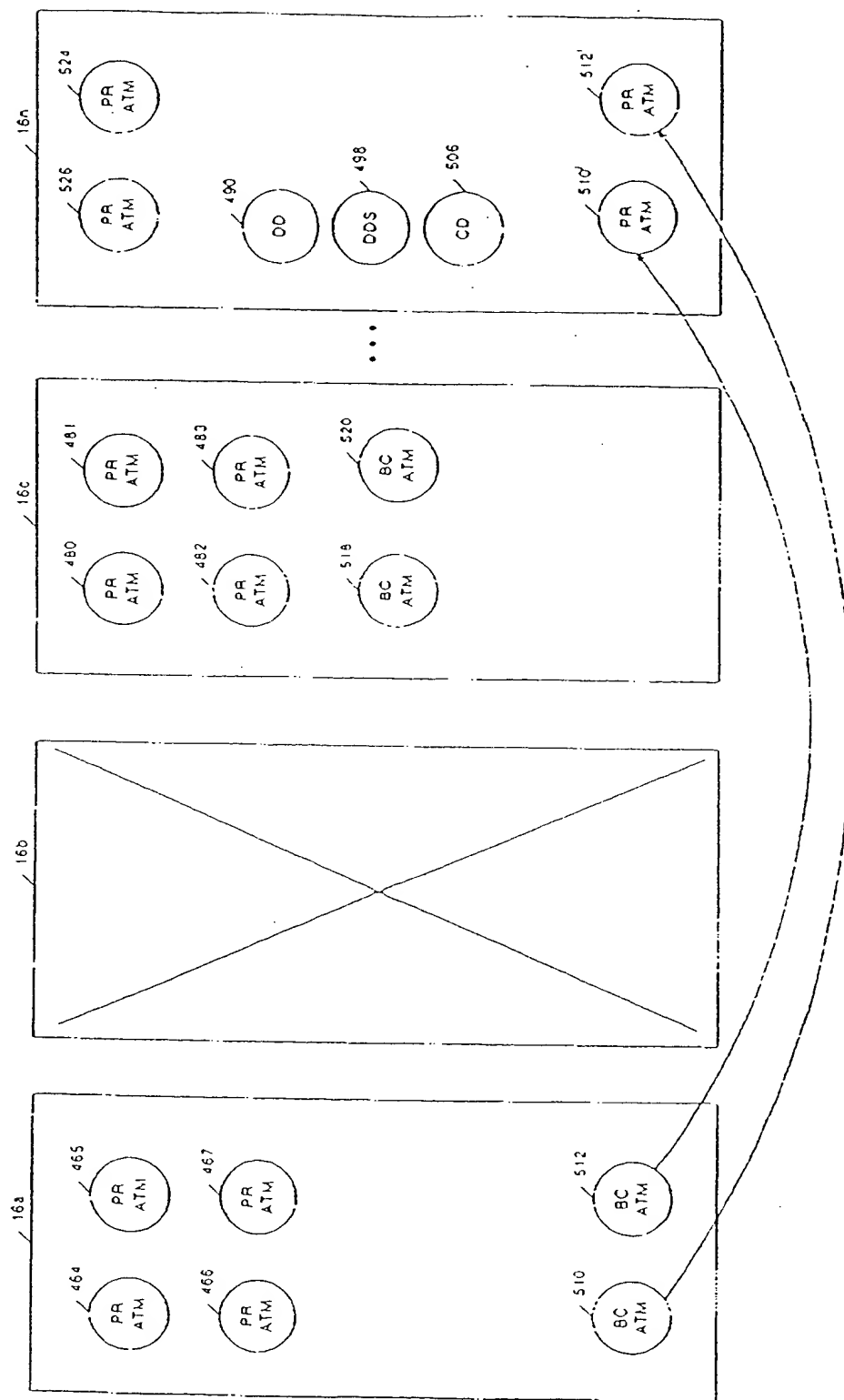
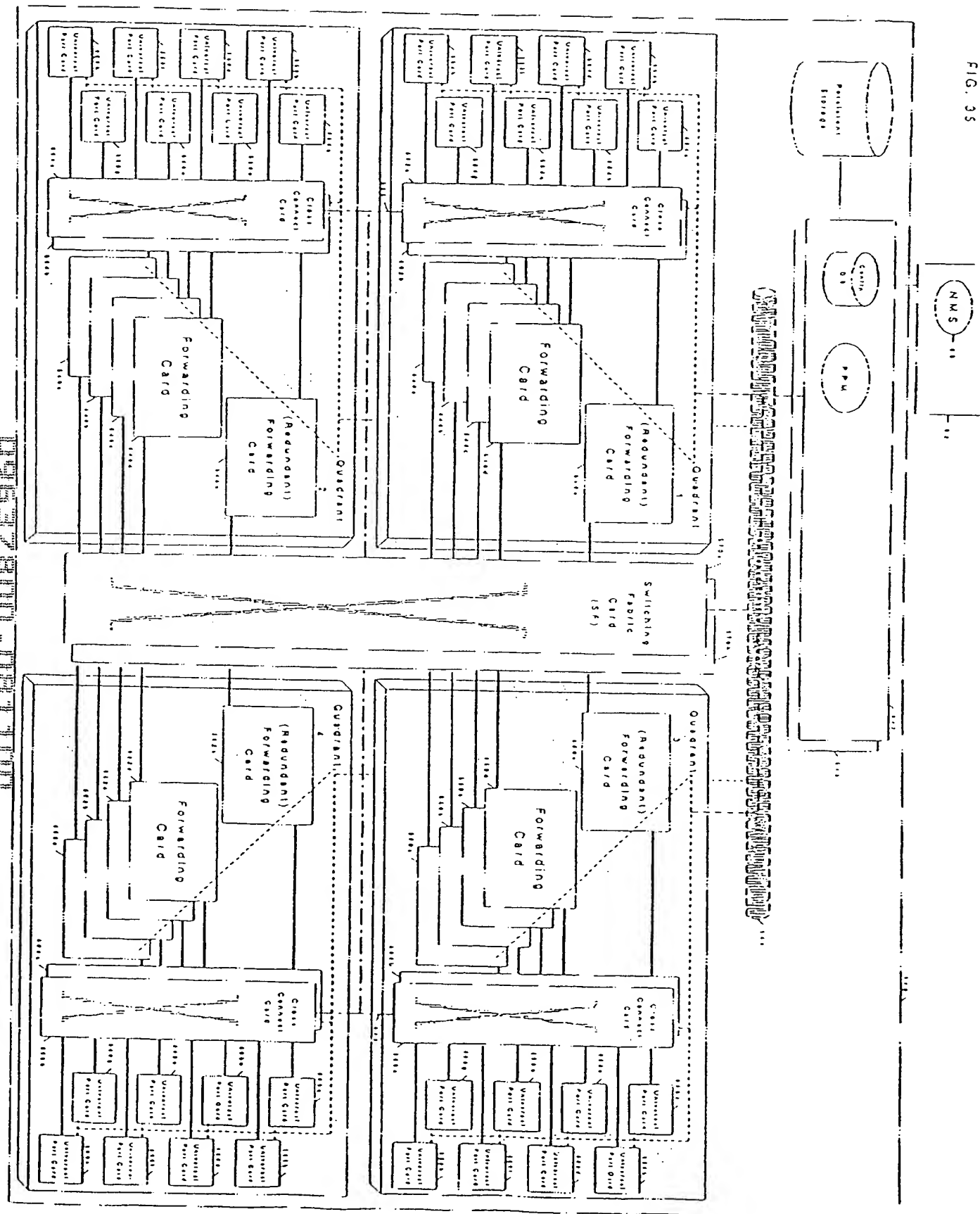
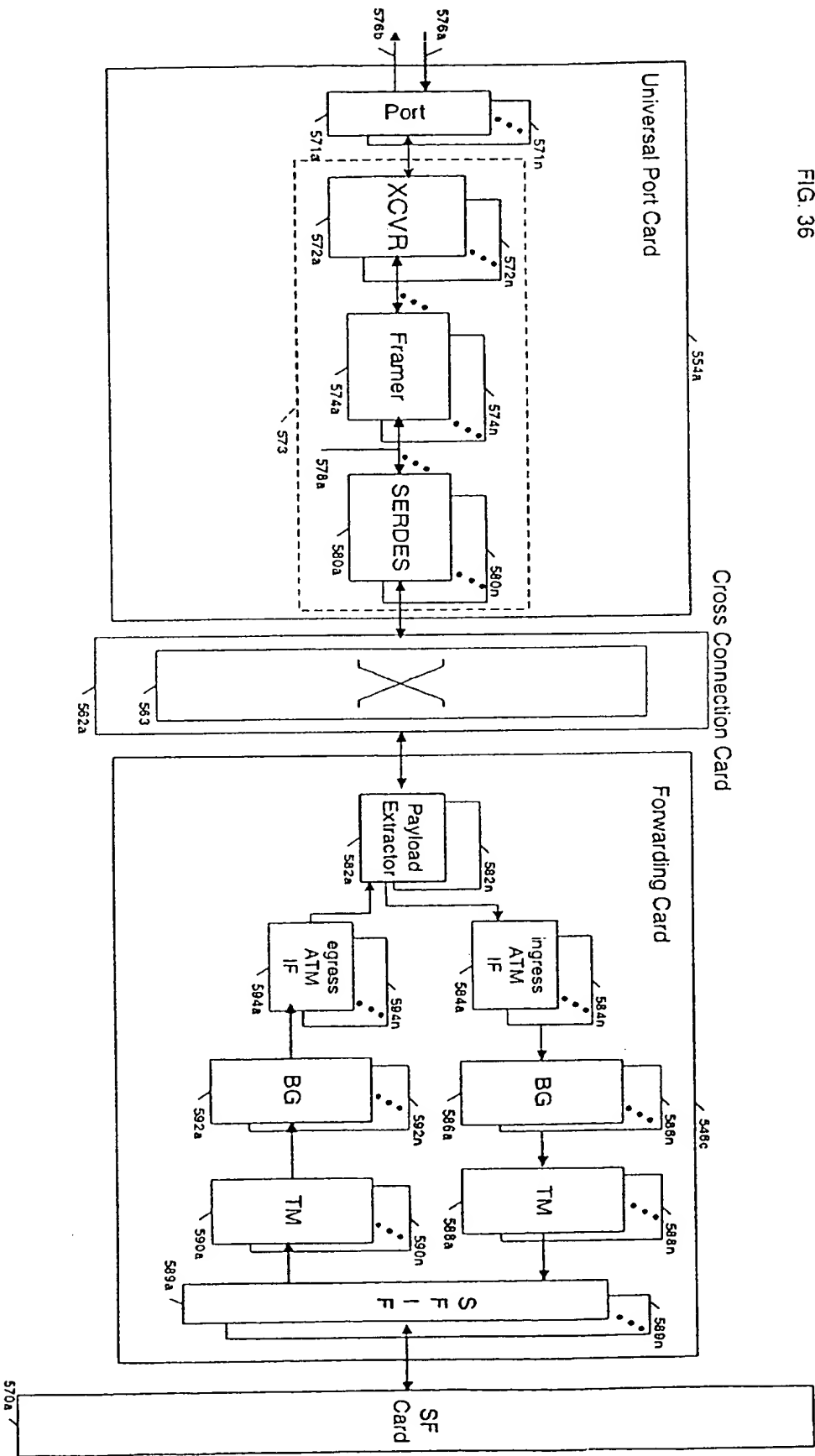


FIG. 35



SECRET

FIG. 36



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FIG. 37

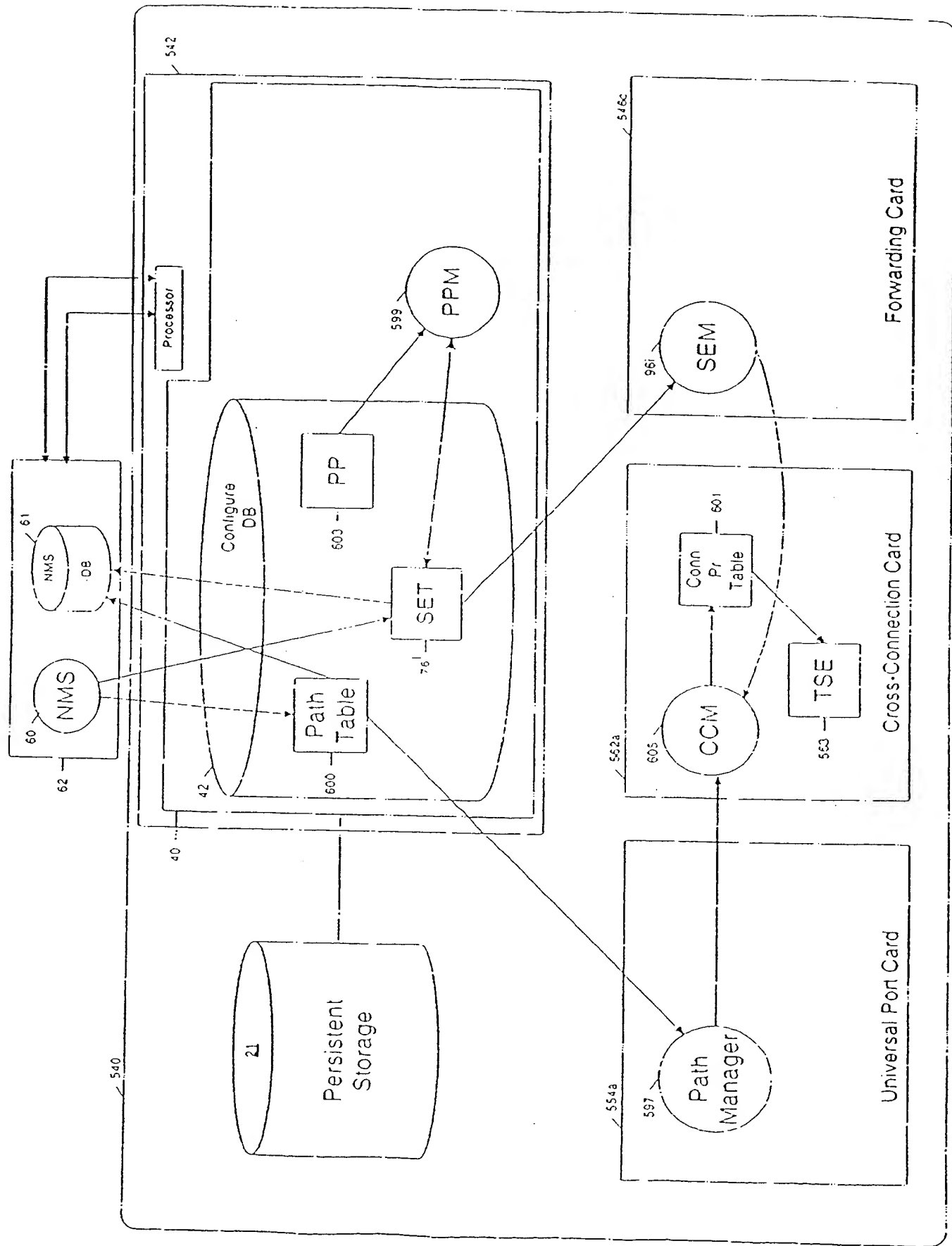


FIG. 38

Path Table 600



Path LID	UP Port LID	Time Slot	# of Time Slots	...
1666	1231	4	3	
...

602

00F50" 0082E960

FIG. 39

Service End Point Table 76'

SE #	Q #	FC LID	FC Slice	FC Time Slot	Path PID	...
878	1				1666	
...

09637800-081100

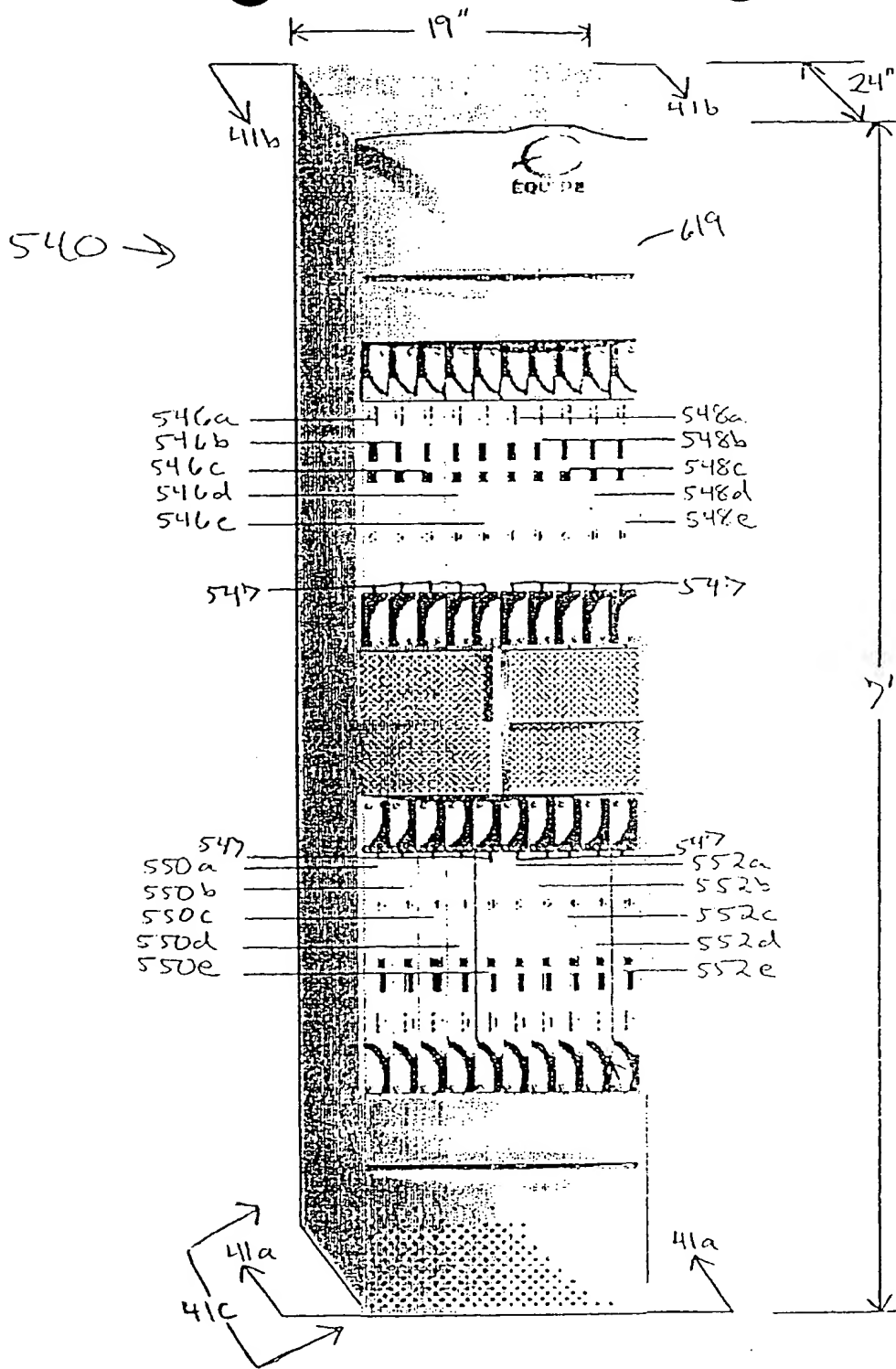


Fig. 40

FIG. 41a

Front

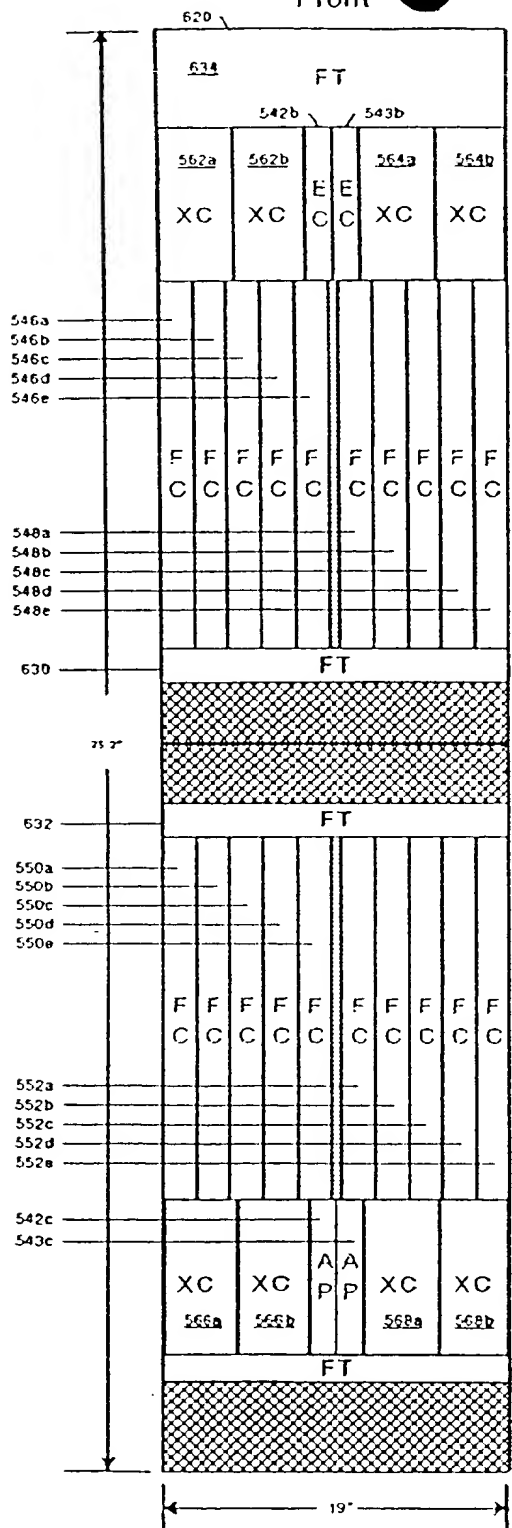
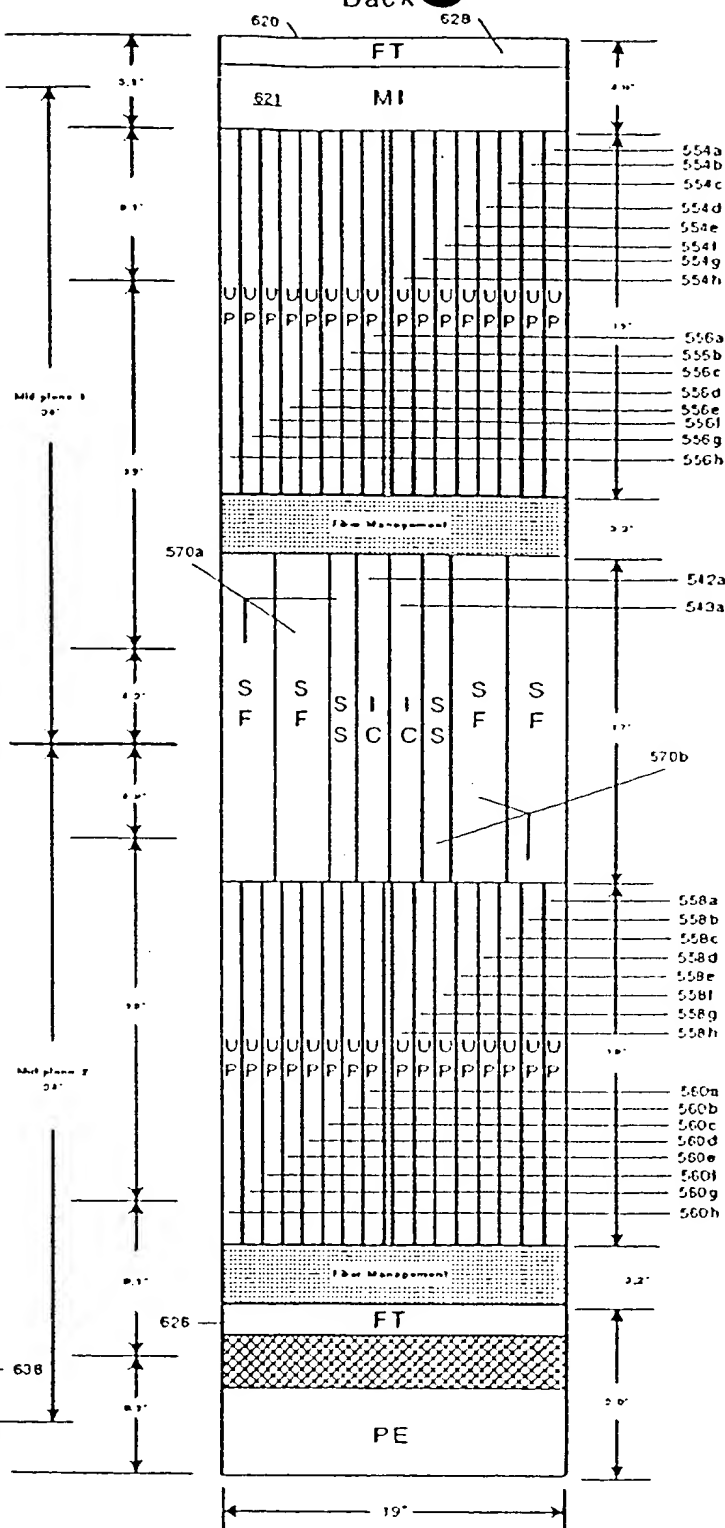


FIG. 4

Back



00T80"0082E950

FIG. 41c
Side

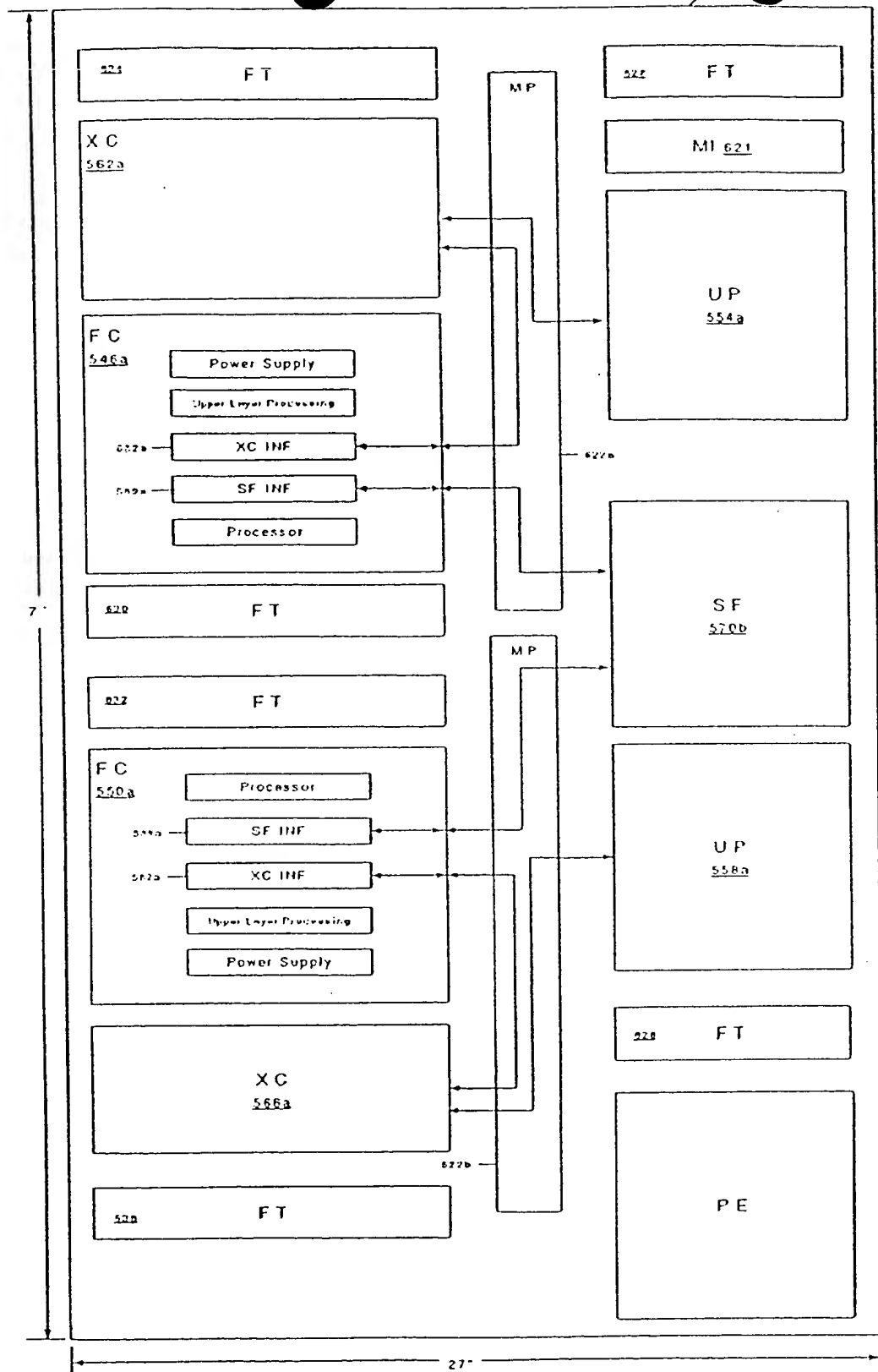
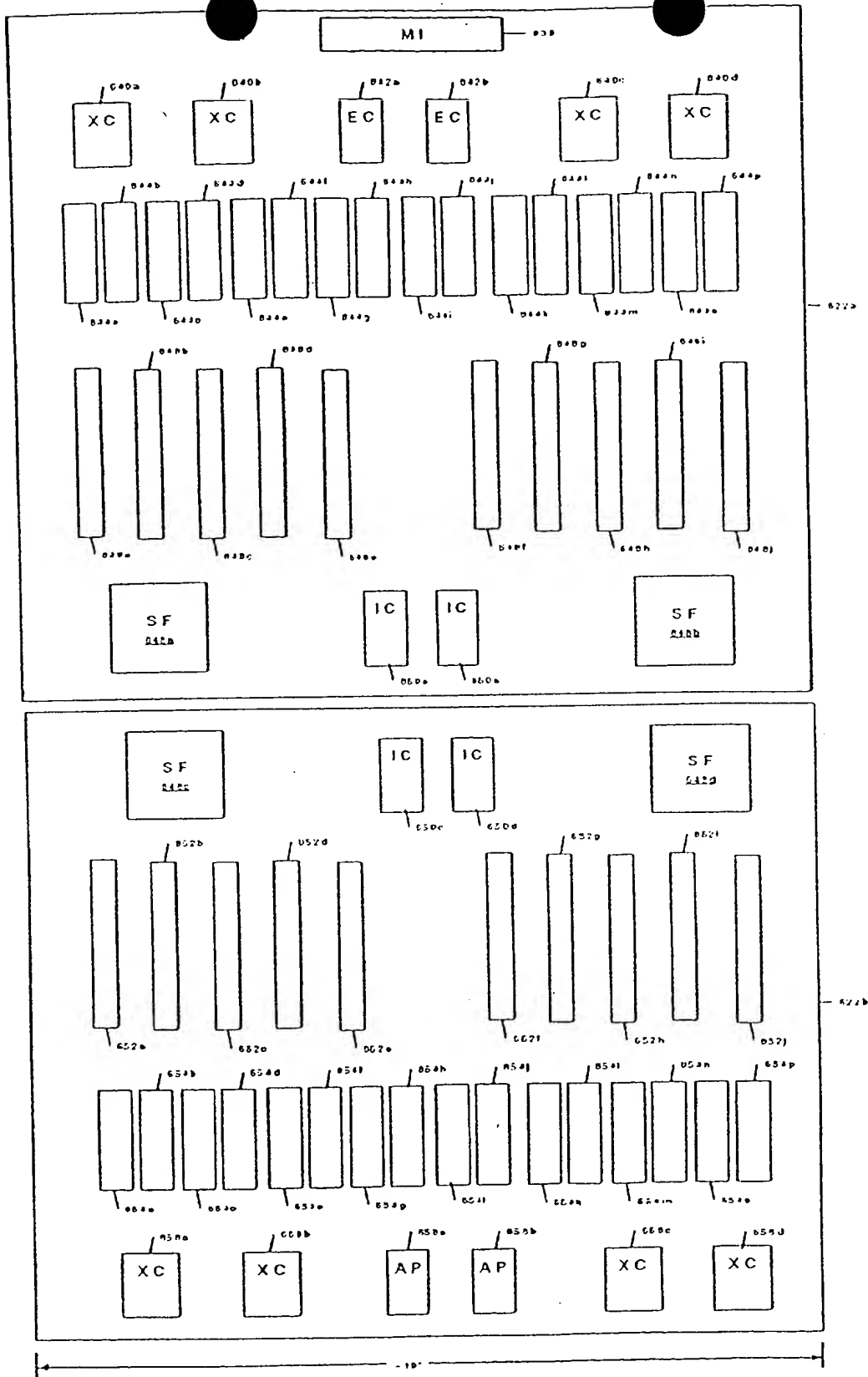
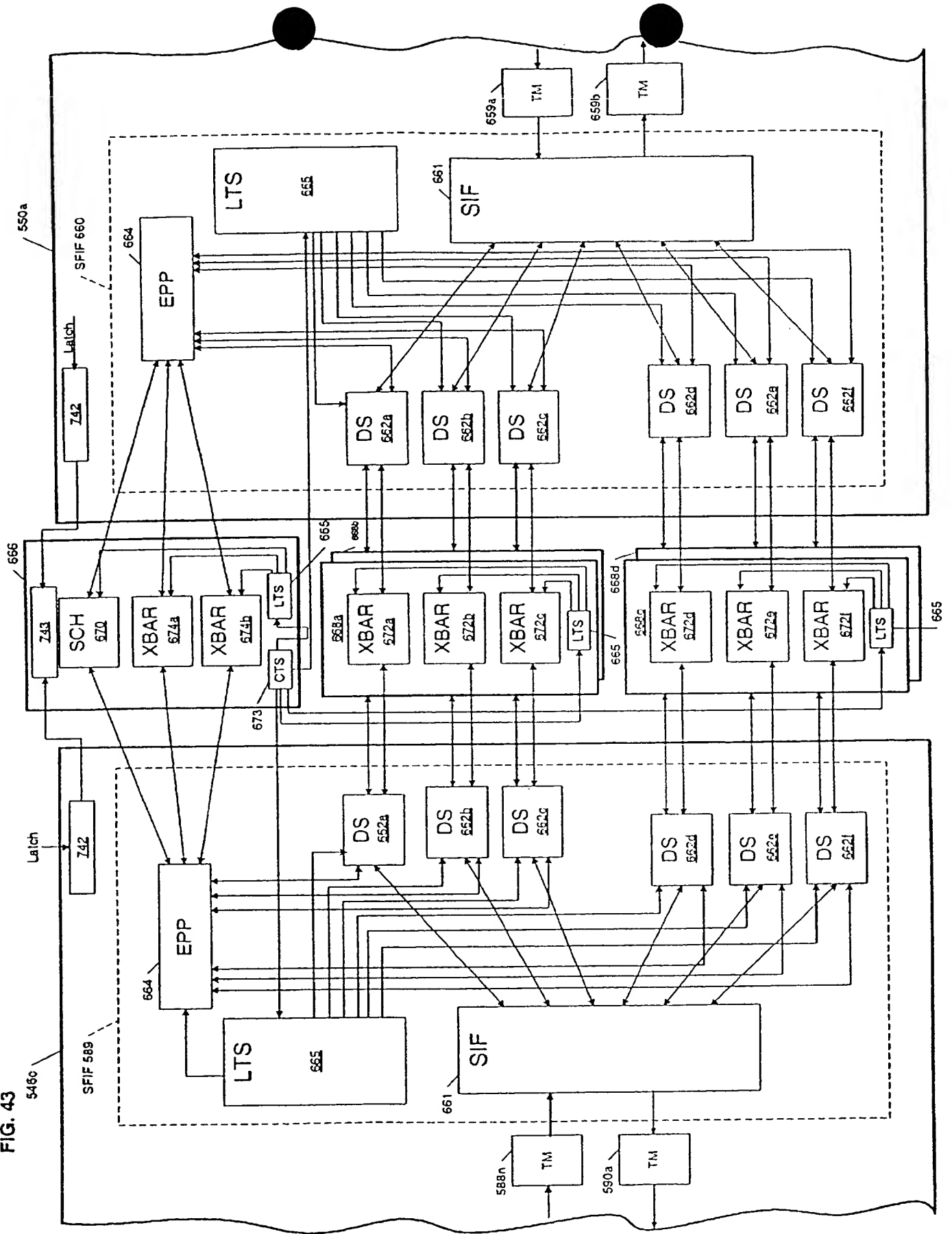


FIG. 42



SECRET



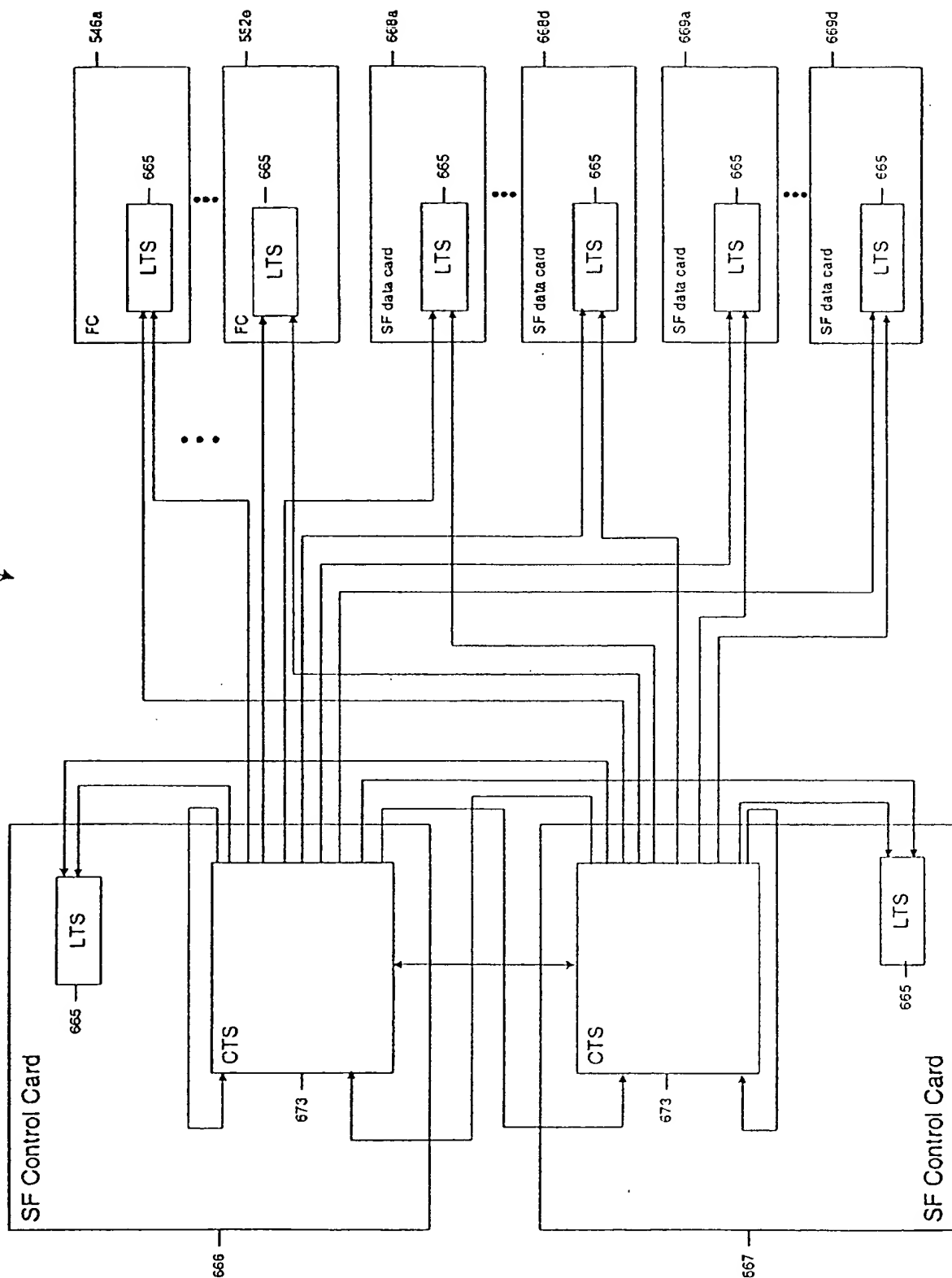
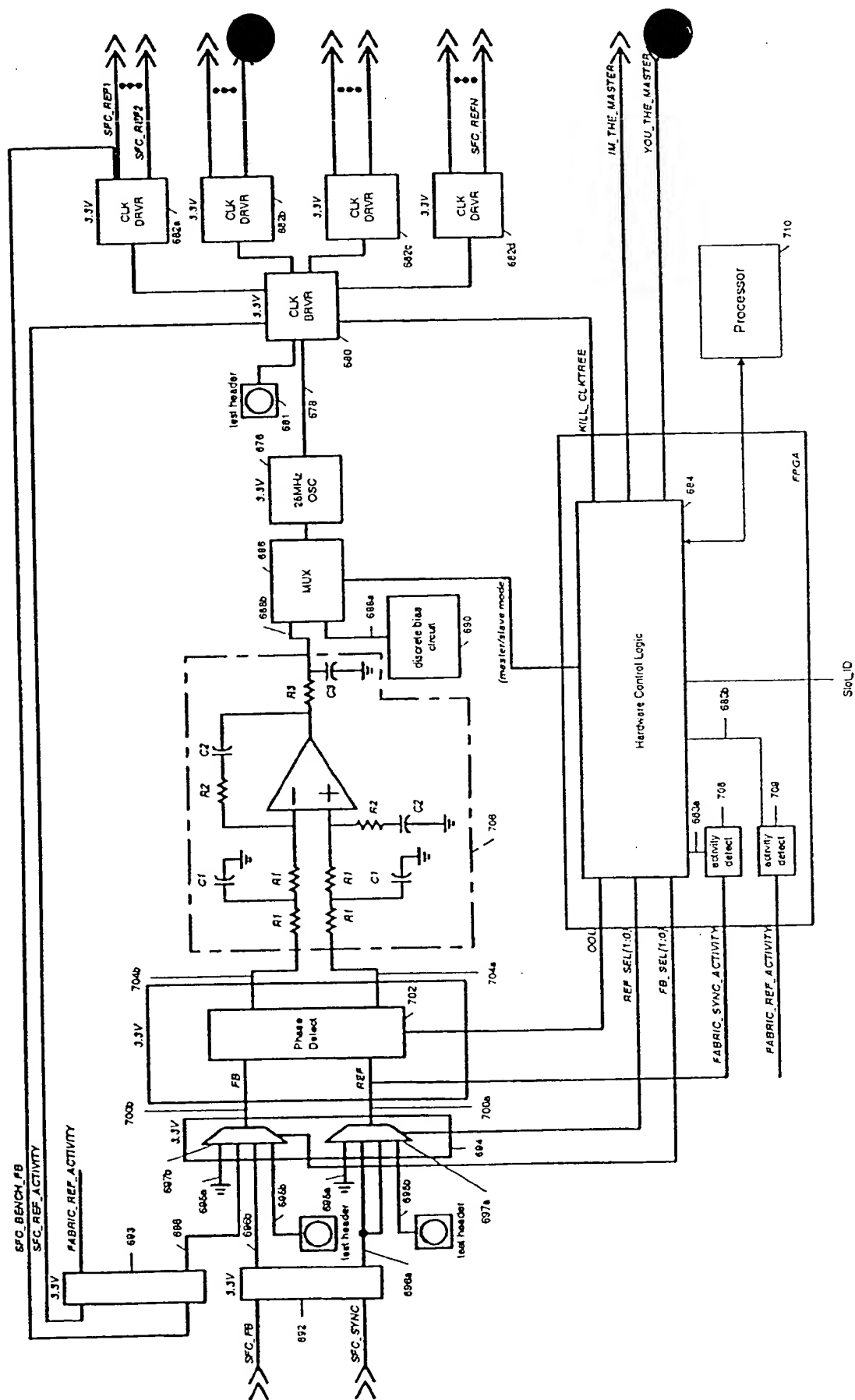


FIG. 44

CTS 673



LTS

665

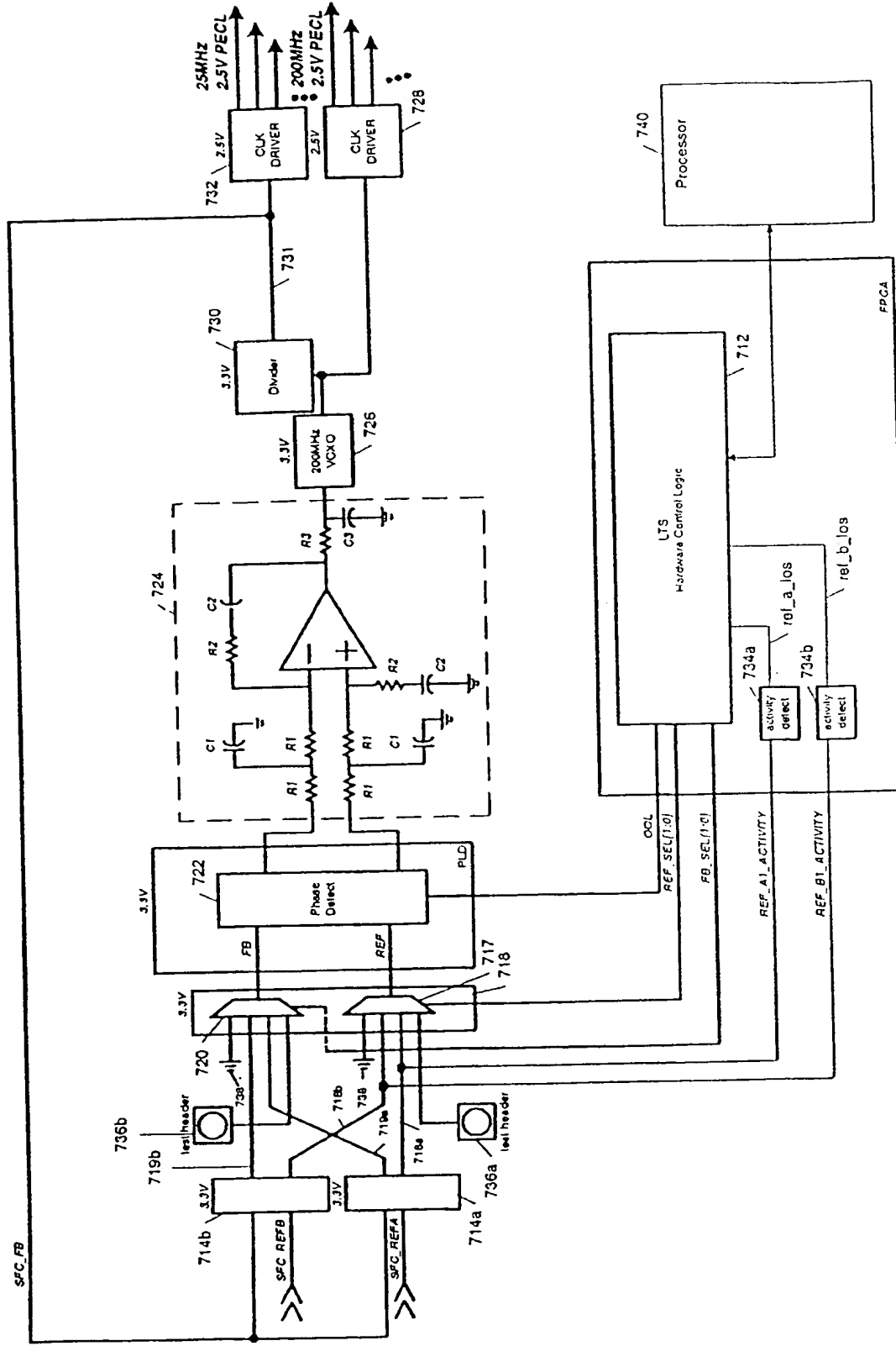


FIG. 47

09-08-07

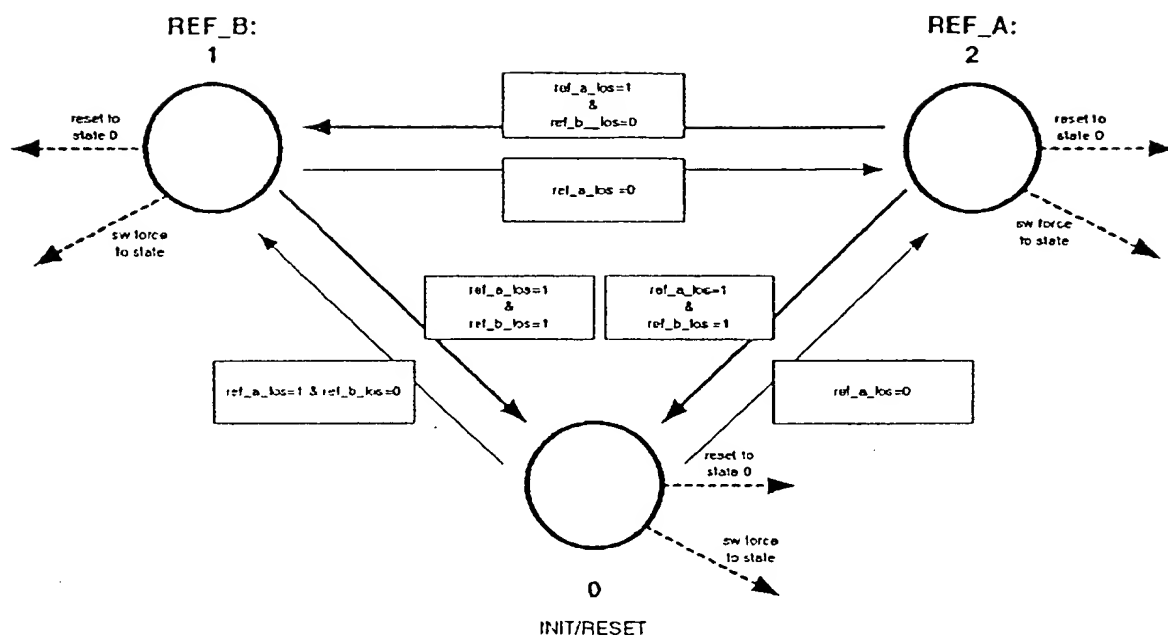


FIG. 48

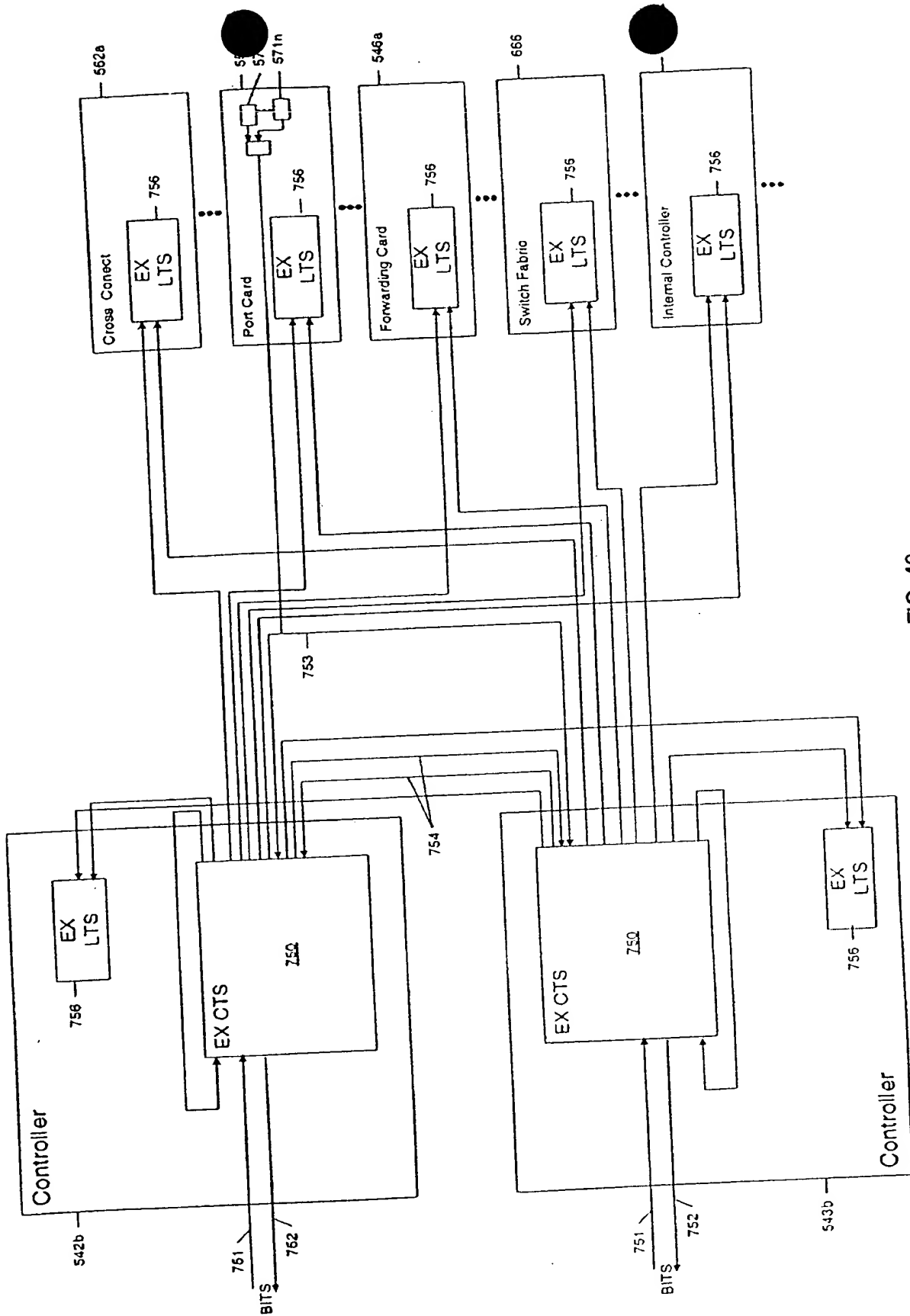


FIG. 49

EX CTS 750



FIG. 50

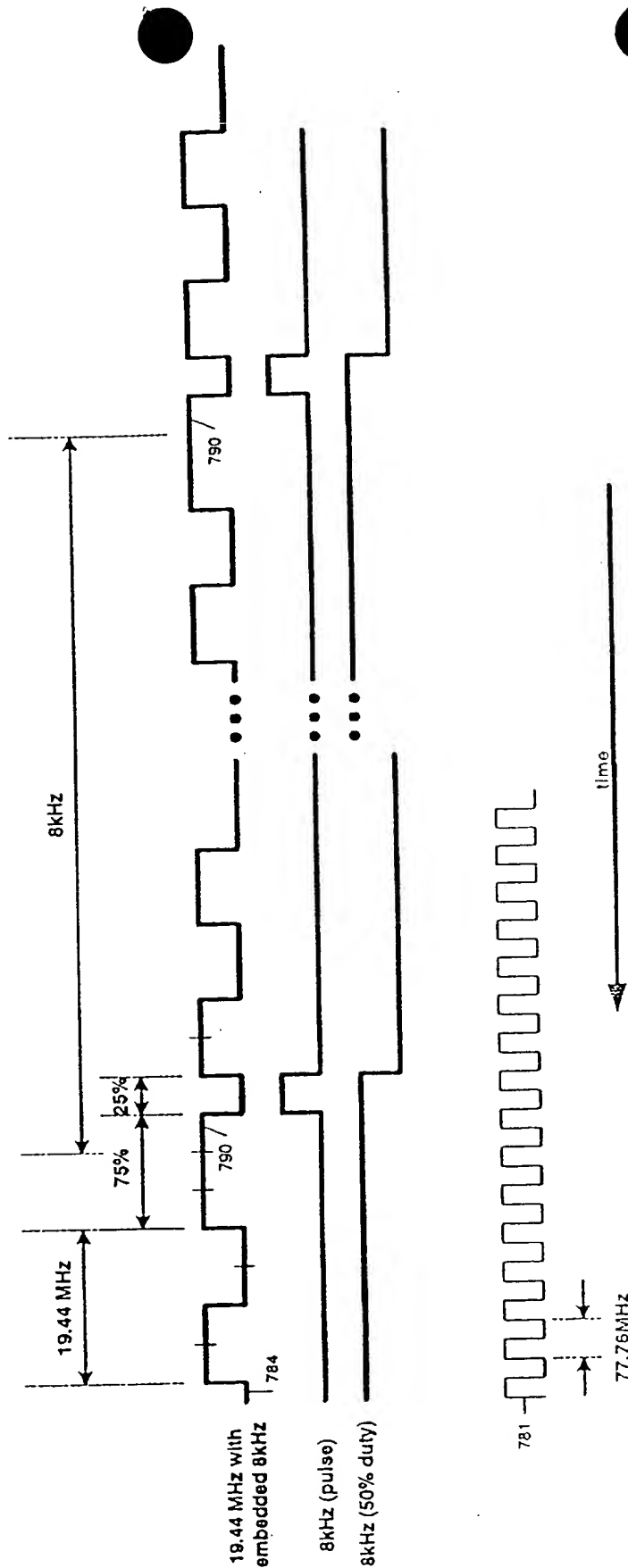


FIG. 51

792

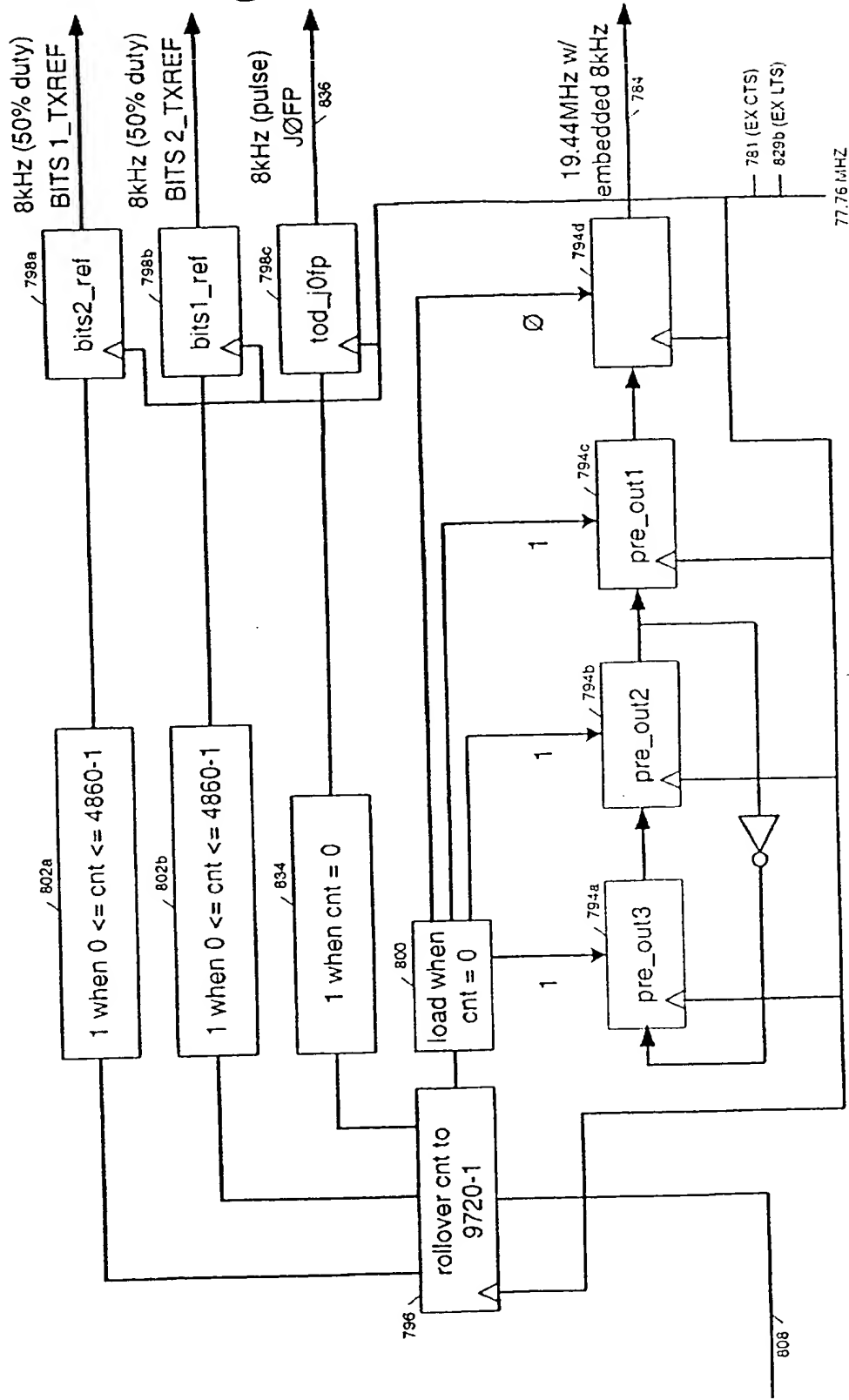
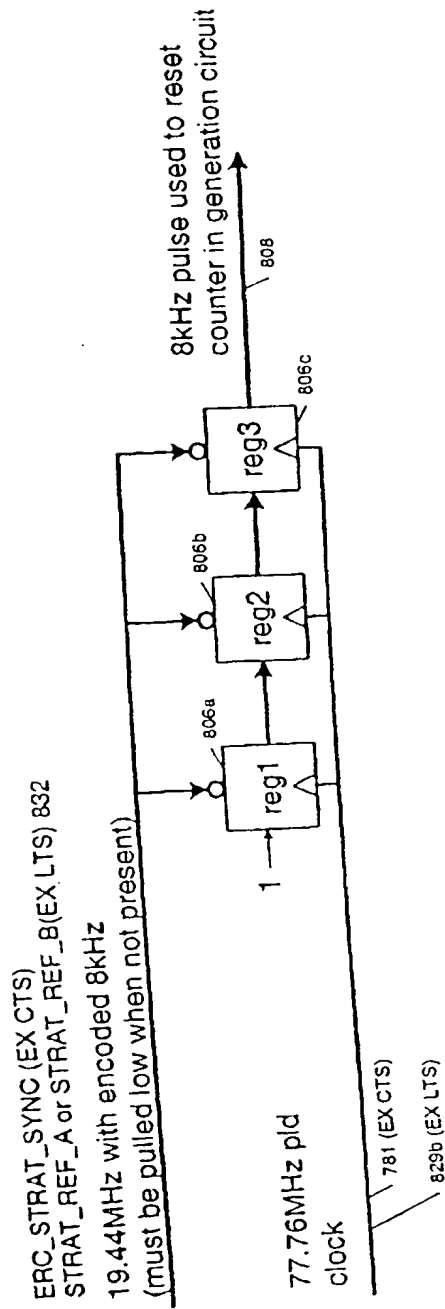


Fig. 52

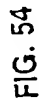
804



Extractor

FIG. 53

EX LTS 756



FPGA

EX CTS 750a

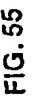


FIG. 55

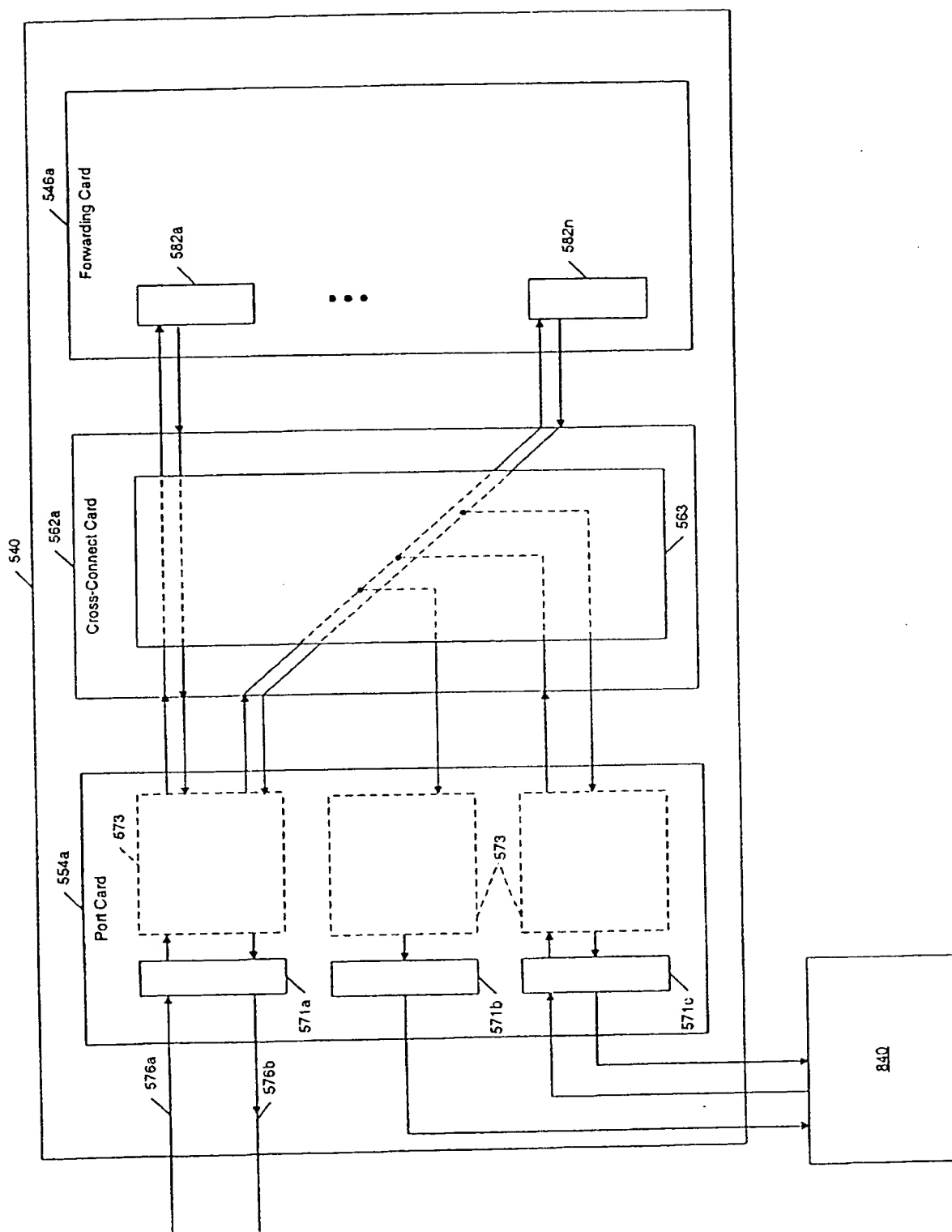


Fig. 56

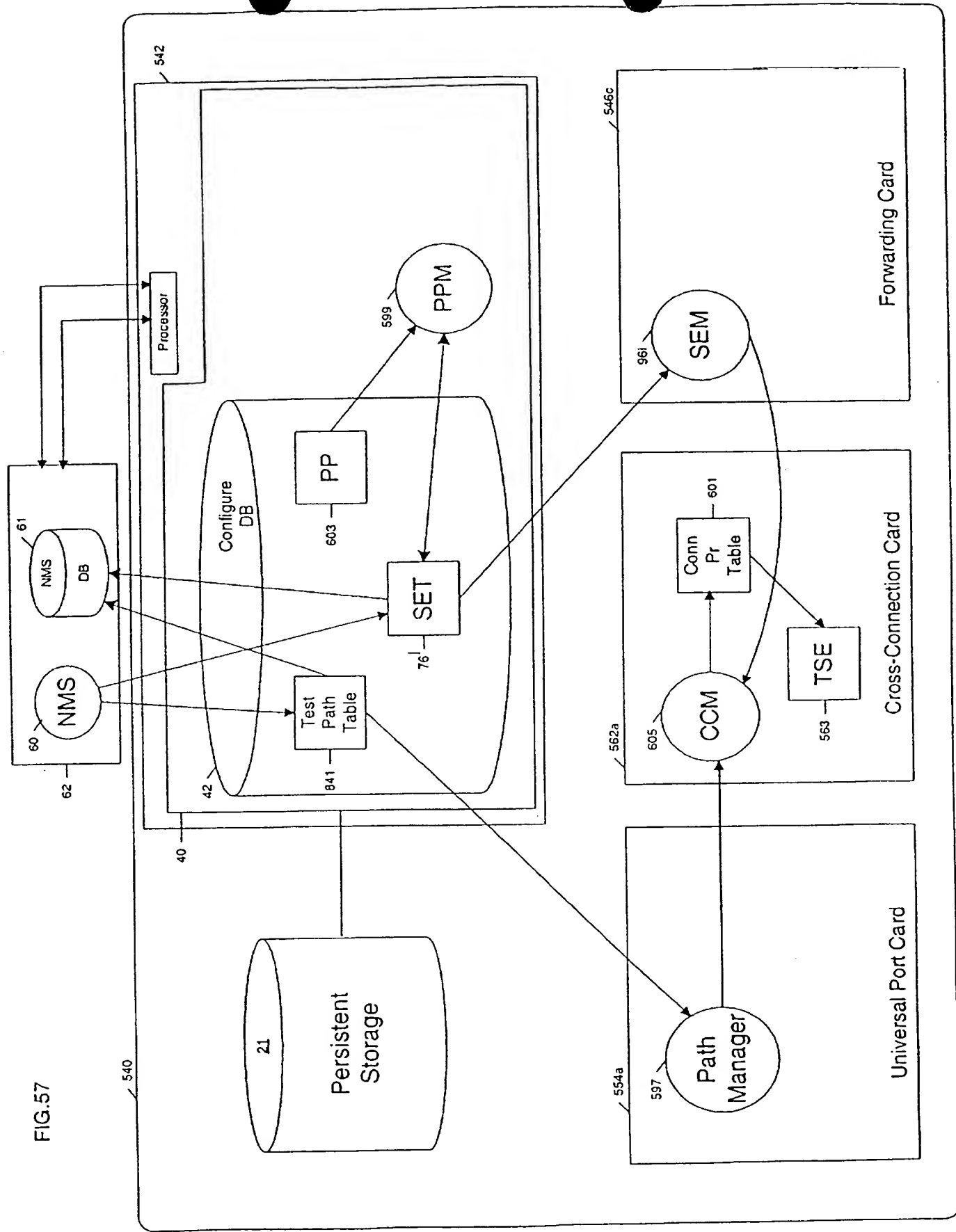


FIG. 58

Test Path Table 841

844		845					
Path LID	UP Port LID	Time Slot	# of Time Slots	Monitor	Enable Port Receiver	...	
842 1666	1232	4	3	Ingress	No		
843 1666	1233	4	3	Egress	No		
844 1666	1233	4	3	Ingress	Yes		
•	•	•	•			•	
•	•	•	•			•	